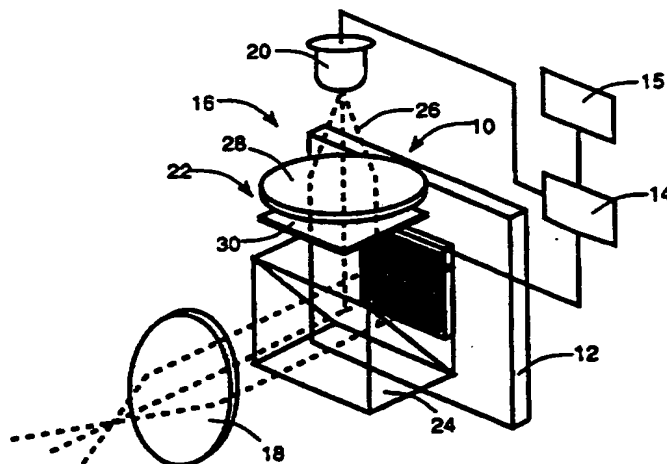




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(54) Title: ACTIVE MATRIX LIQUID CRYSTAL IMAGE GENERATOR



(57) Abstract

A system for producing spatially modulated monochrome or color light having gray scale includes an active matrix liquid crystal spatial light modulator having light modulating means including (i) a layer of ferroelectric liquid crystal material which is designed to switch between ON and OFF states and (ii) active matrix means including VLSI circuitry for dividing the layer of liquid crystal material into an array of individual liquid crystal pixels and for causing each of the pixels of liquid crystal material to modulate light individually by switching between the ON and OFF states in a way that depends upon the data which the VLSI circuitry is written. The system also includes illumination means having a light source for directing light from the source into the pixel-divided layer of ferroelectric liquid crystal material in a specific way. And finally, the system includes means for writing the VLSI circuitry with preselected data in accordance with a particular data ordering scheme such that the circuitry, in response to the written data, causes the pixels of liquid crystal material to individually switch between their ON and OFF states and therefore modulate light from the source in a way which, depending upon the data, produces a specific overall pattern of gray scale light.

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ACTIVE MATRIX LIQUID CRYSTAL IMAGE GENERATOR
GOVERNMENT CONTRACT CLAUSE

This invention was made with Government support under contracts NAS9-18858 and NAS9-19102 awarded by the National Aeronautics and Space Administration and
5 contracts DAA-H01-92-C-R275 and DAA-H01-94-C-R154 awarded by the Advanced Research Projects Agency. The Government has certain rights in this invention.

BACKGROUND OF THE INVENTION

The present invention relates generally to display systems, and more particularly to a miniature display system in which a ferroelectric liquid crystal (FLC)
10 spatial light modulator is positioned directly on top of and controlled by a specifically written (i.e. addressed) very-large-scale-integration (VLSI) silicon integrated circuit (IC) backplane.

In the field of miniature displays and especially those using spatial light modulators, it is well known that stationary and moving images, either monochrome or
15 color, may be sampled and both color separated and gray scale separated. These separations may be digitized forming digitized images which correspond to the given images. These digitized images are used by devices in this field to create visual images that can be used for a direct visual display, a projected display, a printer device, or for driving other devices that use visual images as their input. The present invention
20 provides a novel spatial light modulator using novel data sequencing schemes which may be used to produce a direct view miniature display or a projected display, or for driving a printer or other devices that use visual images as their input.

One of the ongoing challenges facing the manufacture of miniature display systems is providing smaller and smaller displays with higher and higher resolution.
25 The combination of these two factors dictates smaller and smaller pixel sizes in order to achieve smaller displays with higher resolution. Miniature displays which are small enough to be mounted onto a helmet or displays small enough to be supported by a pair of eyeglasses will find a wide variety of uses if they can provide adequate resolution and brightness in a small, low-power package at a low cost. Conventional display
30 technologies such as CRTs are difficult to miniaturize and therefore do not hold much promise in this field. Alternatively, new displays based on VLSI integrated circuits are currently being developed. Examples of these miniature displays in the prior art include digital micromirror devices, active-matrix electroluminescent displays, and active-matrix

liquid crystal displays. However, each of these prior art devices has significant limitations in resolution, brightness, compactness, lumination efficiency, or color capability. Furthermore, because of various physical restrictions on systems of these types, the smallest pixel pitch that to applicant's knowledge has been achievable thus far
5 has been a pitch much greater than 10 microns. As will be seen hereinafter, the present invention provides a display capable of providing smaller pixels, for example with a pitch on the order of 10 microns or less, with higher resolution in a bright, full color capable display. As will also be seen, the present invention provides for unique data ordering schemes, and a unique method of illuminating the display, as well as other
10 features which will become apparent.

SUMMARY OF THE INVENTION

As will be described in more detail hereinafter, a system for producing modulated monochrome or color light having gray scale is disclosed. The system includes an active matrix liquid crystal spatial light modulator having light modulating
15 means including (i) a layer of ferroelectric liquid crystal material which is designed to switch between ON and OFF states and (ii) active matrix means including VLSI circuitry for dividing the layer of liquid crystal material into an array of individual liquid crystal pixels and for causing each of the pixels of liquid crystal material to modulate light individually by switching between the ON and OFF states in a way that depends upon
20 the data with which the VLSI circuitry is written. The system also includes illumination means having a light source for directing light from the source into the pixel-divided layer of ferroelectric liquid crystal material in a specific way. And finally, the system includes means for writing the VLSI circuitry with preselected data in accordance with a particular data ordering scheme such that the circuitry, in response to the data, causes
25 the pixels of liquid crystal material to individually switch between their ON and OFF states and therefore modulate light from the source in a way which, depending upon the data, produces a specific overall pattern of gray scale light.

In accordance with one feature of the present invention, simplified VLSI circuitry, specifically circuitry using a single active component per pixel, is combined
30 with a very thin layer of ferroelectric liquid crystal material, approximately one micron thick, to produce a spatial light modulator for a display in which the spatial light modulator is comprised of extremely small pixels. This feature of the present inventions

allows a spatial light modulator with a pixel pitch of 10 microns or less to be produced. In a specific embodiment disclosed herein, the spatial light modulator includes a thin layer of ferroelectric liquid crystal confined between a silicon VLSI circuitry backplane and a glass window coated on its inner side with a transparent electrode layer of indium-tin oxide. The VLSI backplane includes an array of conductive metal pads positioned on the upper surface of the VLSI backplane. The conductive metal pads each have a reflective top surface which is designed to reflect light directed into the spatial light modulator back out of the spatial light modulator and also act as electrodes controlled by the data writing means. The conductive metal pad electrodes and the transparent electrode layer are positioned on opposite sides of the ferroelectric liquid crystal layer and are used to form electric fields through the layer of individually controllable ferroelectric liquid crystal pixels whose positions correspond to the positions of the conductive metal pad electrodes.

In accordance with another feature, different very specifically configured data ordering schemes compatible with the simplified VLSI circuitry are utilized in order to achieve controllable gray scale. Each of these data ordering schemes contemplates first establishing a particular frame rate for the display system. Each of these frames is then divided into a plurality of subframes for purposes of obtaining monochromatic gray scale in the case of a monochromatic display or subframes for purposes of obtaining color and sub-subframes for obtaining color gray scale in the case of a color display.

In accordance with one data ordering scheme, the gray scale of any given pixel is obtained by dividing each frame into subframes of time periods, preferably time periods of unequal length, while maintaining the light directed into the modulator at a fixed brightness and by either turning ON or OFF the particular pixel during certain subframes of the frame such that the cumulative time in which the pixel is ON during the frame is proportional to the desired gray scale for that pixel. Alternatively, in accordance with a second data ordering scheme, the gray scale of any given pixel is obtained by dividing each frame into subframes of equal time periods and by turning ON or OFF the particular pixel during certain subframes of the frame while at the same time changing the brightness of light directed into the spatial light modulator for the different subframes. The cumulative time in which the pixel is ON, in combination with the brightness of the light during each subframe, is proportional to the desired gray scale for that pixel. A third scheme is a combination of the first scheme and the second scheme.

In each of these schemes, because of the frame rate, the viewer's eye perceives the time integral of brightness of each pixel for each frame, thereby perceiving the desired gray scale image. In both of these second and third data ordering schemes, additional blackout subframes are provided in accordance with the present invention in order to
5 maintain the fidelity of the ultimately produced display.

In accordance with another feature of the present invention, the three different data ordering schemes are utilized in order to achieve a color display with gray scale for each color. This is done by establishing a particular frame rate for the display system and dividing the frames into a plurality of subframes for purposes of obtaining
10 color. During each of these subframes light of a certain color is directed into the spatial light modulator. The subframes are further divided into a plurality of sub-subframes for purposes of obtaining gray scale for each color subframe, as indicated above. In all three of these color schemes, blackout subframes and/or blackout sub-subframes may be used. In accordance with still another feature of the present invention, individual light emitting
15 diodes are used as color light sources for a color display.

In accordance with a more general embodiment of the present invention, a system for producing modulated light is disclosed including a spatial light modulator having an array of individual light modulating pixels. Each pixel includes a light modulating medium which is designed to switch between ON and OFF light modulating
20 states for modulating light depending upon the pixels light modulating state, and means for switching the pixels associated light modulating medium between the medium's ON and OFF states in response to and dependent on externally generated, preselected data signals. In one example of this embodiment, the modulating medium is ferroelectric liquid crystal which is switched using a digital approach from between only a fully ON
25 and a fully OFF state. In a second example of this embodiment the modulating medium is nematic liquid crystal which is controllably switched using an analog approach to a fully ON state, a fully OFF state, or any partially ON state. The system also includes means for generating a stream of the preselected data signals and means for applying the preselected data signals to the switching means associated with each of the pixels during
30 each one of a series of successive time periods such that (i) for any given successive time period, all of the pixels are first caused to modulate light depending upon the states of their respective light modulating mediums and responsive to the data signals applied to the light modulating mediums during the given period, and (ii) thereafter during

certain ones of the time periods, the entire array of pixels are switched to the OFF state, all at the same time, whereby to provide a pixel blackout subperiod during each of the certain time periods.

BRIEF DESCRIPTION OF THE DRAWINGS

5 The features of the present invention may best be understood by reference to the following description of the presently preferred embodiments together with the accompanying drawings in which:

Figure 1 is a diagrammatic perspective view of a miniature display system designed in accordance with the present invention;

10 Figure 2 is a diagrammatic view illustrating one preferred embodiment of a light source forming part of the display system shown in Figure 1;

Figure 3A is a diagrammatic view of the FLC/VLSI spatial light modulator forming part of the display system shown in Figure 1;

15 Figure 3B is a diagrammatic cross sectional view of the FLC/VLSI spatial light modulator shown in Figure 3A;

Figure 3C is a diagrammatic cross sectional view of one of the pixels of the spatial light modulator shown in Figure 3A, with the pixel being shown in both an ON state and an OFF state;

20 Figure 4 is a schematic diagram of the circuitry for a three pixel by three pixel array VLSI backplane of the FLC/VLSI spatial light modulator shown in Figure 3A;

25 Figure 5 is a graph illustrating the ON/OFF state of the pixel in the first row and first column of the array shown in Figure 4 for a period of time equal to two frames with the frames being divided into subframes of different lengths of time for purposes of obtaining gray scale by controlling the ON/OFF state of the pixel during each subframe;

30 Figure 6 is a graph illustrating the ON/OFF state of all of the pixel rows in the array shown in Figure 4 for a period of time equal to two frames with the frames being divided into subframes of different lengths of time while maintaining a constant light source brightness for purposes of obtaining gray scale by controlling the ON/OFF state of the pixels during each subframe;

Figure 7 is a graph illustrating the ON/OFF state of the pixel in the first

row and first column of the array shown in Figure 4 for a period of time equal to two frames with the frames being divided into subframes of equal lengths of time for purposes of obtaining gray scale by controlling the ON/OFF state of the pixel during each subframe and by changing the brightness of light illuminating the array for each of the subframes, each of the subframes being followed by a blackout subframe to allow for changing the brightness of the light without adversely effecting the desired gray scale;

Figure 8 is a graph illustrating the ON/OFF state of all of the pixel rows in the array shown in Figure 4 for a period of time equal to one frame with the frame being divided into subframes of equal lengths of time for purposes of obtaining gray scale by controlling the ON/OFF state of the pixels during each subframe and by varying the brightness of light illuminating the array during each of the subframes, each of the subframes being followed by a blackout subframe to allow for changing the brightness of the light without adversely effecting the desired gray scale;

Figure 9 is a graph illustrating the ON/OFF state of the pixel in the first row and first column of the array shown in Figure 4 for a period of time equal to two frames with the frames being divided into subframes of equal lengths of time for purposes of obtaining gray scale by controlling the ON/OFF state of the pixel during each subframe and by changing the brightness of light illuminating the array for each of the subframes, the light illuminating the display being turned OFF during certain times to allow the pixels to change their ON/OFF state without adversely effecting the desired gray scale before turning ON the light at a different brightness;

Figure 10 is a graph illustrating the ON/OFF state of all of the pixel rows in the array shown in Figure 4 for a period of time equal to one frame with the frame being divided into subframes of equal lengths of time for purposes of obtaining gray scale by controlling the ON/OFF state of the pixels during each subframe and by varying the brightness of light illuminating the array during each of the subframes, the light illuminating the display being turned OFF during certain times to allow the pixels to change their ON/OFF state without adversely effecting the desired gray scale before turning ON the light at a different brightness;

Figure 11 is a graph illustrating the ON/OFF state of the pixel in the first row and first column of the array shown in Figure 4 for a period of time equal to one frame with the frame being divided according to a combination of the schemes illustrated

in Figures 5 and 7;

Figure 12 is a graph illustrating the ON/OFF state of all of the pixel rows in the array shown in Figure 4 for a period of time equal to one frame with the frame being divided according to a combination of the schemes illustrated in Figures 6 and 8;

5 Figure 13 is a graph illustrating the ON/OFF state of the pixel in the first row and first column of the array shown in Figure 4 for a period of time equal to one frame with the frame being divided into subframes for purposes of obtaining color and each of the color subframes being divided into sub-subframes for purposes of obtaining gray scale color by controlling the ON/OFF state of the pixels during each subframe;

10 Figure 14 is a graph illustrating the ON/OFF state of all of the pixel rows in the array shown in Figure 4 for a period of time equal to one frame with the frame being divided into subframes for purposes of obtaining color and each of the color subframes being divided into sub-subframes for purposes of obtaining gray scale color by controlling the ON/OFF state of the pixels during each subframe using the hybrid
15 scheme;

Figure 15 is a graph illustrating the ON/OFF state of all of the pixel rows in the array shown in Figure 4 for a period of time equal to one frame with the frame being divided into subframes for purposes of obtaining color and each of the color subframes being divided into sub-subframes for purposes of obtaining gray scale color
20 by controlling the ON/OFF state of the pixels during each subframe using the time domain scheme only; and

Figures 16A-C are graphs illustrating the illuminator output efficiencies for each of the various gray scale schemes shown in Figures 6, 8, and 12.

DETAILED DESCRIPTION OF THE PREFERRED EMBODIMENTS

25 Referring initially to Figure 1, a preferred embodiment of a miniature display system designed in accordance with the present invention and generally designated by reference numeral 10 is illustrated. As will be described in more detail hereinafter, the display system includes a ferroelectric liquid crystal VLSI (FLC/VLSI) spatial light modulator 12; a data writing arrangement 14 for controlling FLC/VLSI
30 spatial light modulator 12; a video or digitized image source 15 which creates or provides, as an input to data writing arrangement 14, digitized images; an illumination arrangement generally designated by reference numeral 16 for illuminating spatial light

modulator 12; and an appropriately designed readily available viewing eyepiece lens 18. As will also be described in more detail hereinafter, FLC/VLSI spatial light modulator 12 includes an array of individually addressable pixels, not shown in Figure 1, designed to be switched by data writing arrangement 14 between ON (light) and OFF (dark) states. Illumination arrangement 16 includes a light source 20 which may be switchably controlled by data writing arrangement 14, a collimating arrangement 22, and a polarizer/analyzer 24.

Still referring to Figure 1, either unpolarized or polarized light, generated by light source 20 in the form of light rays 26 is collected by collimating arrangement 22 and directed into polarizer/analyzer 24. The polarizer/analyzer 24 causes light of a particular polarization state, for example S-polarized light, to be directed into FLC/VLSI spatial light modulator 12 while any light of the opposite polarization state, for example P-polarized light is lost. The polarized light directed into FLC/VLSI spatial light modulator 12 is reflected back to polarizer/analyzer 24 by the individual pixels of the spatial light modulator with the light's polarization state being either maintained (for example S-polarized) or changed (for example P-polarized) depending on the ON/OFF state of the individual pixels of FLC/VLSI spatial light modulator 12. For the pixels which are in the ON state, the polarization of the light is changed by the FLC which allows the light to pass through polarizer/analyzer 24 into eyepiece lens 18 presenting a bright pixel in the array of pixels to a viewer of the display. For the pixels which are in the OFF state, the light's polarization is maintained, causing the polarizer/analyzer 24 to direct the light back up toward the light source or away from eyepiece lens 18, thereby presenting a dark pixel to the viewer. Thus, at any particular instant in time, when a given pixel is fully OFF or ON, it is as dark as it can be (OFF) or as bright as it can be (ON). Therefore, in order to obtain gray scale, it is necessary to modulate or switch the pixels between the OFF and ON states in a binary fashion, as will be seen hereinafter.

Although the system described above utilizes a polarizer/analyzer to polarize the light, it should be understood that this is not a requirement of the present invention. Instead, the present invention contemplates utilizing polarized light which would eliminate the need for a polarizer. Therefore, the present invention would apply regardless of whether polarized or unpolarized light is directed into the spatial light modulator of the present invention.

Still referring to Figure 1, the various components of illuminator arrangement 16 will now be described in detail. As was mentioned above, in the presently preferred embodiment of the present invention, illuminator arrangement 16 includes polarizer/analyzer 24. In this particular embodiment, polarizer/analyzer 24 is a polarizing beam splitting (PBS) cube designed to function over a wide spectrum of light such that it retains its polarizing and analyzing characteristics for substantially the entire visible light spectrum. In the embodiment illustrated in Figure 1, light from light source 20 is directed down into PBS cube 24 which reflects only the light having a specific polarization into FLC/VLSI spatial light modulator 12. Light not having the specific polarization is allowed to pass through the PBS cube and is not directed into the modulator. In this embodiment, the PBS cube also acts as the analyzer for the light reflected back out from spatial light modulator 12. As described above, if the polarization of the light has been changed by the FLC it is allowed to pass through the PBS cube into eyepiece 18 forming a bright pixel. If it has not been changed by the FLC the light is reflected back up toward light source 20, thus presenting a dark pixel to the viewer.

Although the polarizer/analyzer has been described as a polarizing beam splitting cube for the embodiment shown in Figure 1, it should be understood that the polarizer/analyzer may take on a wide variety of forms. For instance, depending on the configuration of the display, the polarizer/analyzer may be provided in the form of three separate components, one for polarizing the light, one for directing it into the modulator, and one for analyzing the light directed out from the modulator as described above. Also, the polarizer/analyzer has been described as operating over a wide spectrum of light.

This is not a requirement of the present invention and for instance would not be necessary for a monochrome display which only made use of a certain color of light. However, in the case of color, it has been specifically selected to act upon the entire visible spectrum and therefore overall system 10 requires but a single polarizer/analyzer regardless of the number of discrete colors utilized.

As mentioned above, illumination arrangement 16 includes collimating arrangement 22. Figure 1 depicts collimating arrangement 22 as being a collimating lens 28 and an illuminance profile filter 30 for producing uniform lumination. These components may take on a wide variety of forms including novel optical systems

disclosed in copending United States Patent Application Serial Number 08/362,234 Attorney Docket Number DIS1P005 entitled OPTICS ARRANGEMENTS INCLUDING LIGHT SOURCE ARRANGEMENTS FOR AN ACTIVE MATRIX LIQUID CRYSTAL IMAGE GENERATOR filed cotemperaneously herewith, which application
5 is incorporated herein by reference.

Referring now to Figure 2, light source 20 will be described in detail. Light source 20 may take a wide variety of forms depending on the desired overall display system requirements, in particular, color and brightness requirements. In one preferred embodiment of the present invention, for a color version of the miniature
10 display system, light source 20 includes three light emitting diodes (LEDs), a red LED 32, a green LED 34, and a blue LED 36. Each of these LEDs may be turned ON and OFF individually and may be turned ON with controllable brightness in which case a variable power supply generally designated at 37 is provided. Alternatively, as shown in Figure 1 light source 20 may be made up of a single LED package containing a
15 plurality of LED die including one or more die of at least two different colors, each of the colors of die being individually switchable and having a controllable brightness. As mentioned above, these colors would typically be red, green, and blue and each would generate an appropriate wavelength of light to allow the mixing of these colors to produce the broadest range of colors possible. By selecting the specific colors of the
20 LED die, the overall color spectrum available in the display may be controlled.

LEDs are particularly well suited to be used as the light source for the miniature display system of the present invention for several reasons. First, they have a very low power requirement relative to the brightness of light generated. Second, they are very compact and can be easily incorporated into an extremely small system. Third,
25 they are very fast switching and can be easily operated at controllable brightness. Fourth, they are available in a variety of colors. And finally they are very reliable and long lived.

Although the light source has been described as including three colors of LEDs with those three colors being red, green, and blue, it should be understood that
30 the present invention is not limited to using three colors or to using these three specific colors, but instead would apply regardless of the number of colors used and regardless of the specific colors selected. Moreover, where the overall system is monochromatic, only one LED would be necessary. Also, although the light source has been described

as one or more LEDs, it should be understood that any other type of light sources such as, but not limited to, laser diodes, fluorescent lamps, electroluminescent lamps, gas discharge arc lamps, incandescent bulbs, or cathodoluminescent sources of the field emitter or cold cathode type would fall within the scope of the present invention.

5 In an alternative embodiment of the present invention, light source 20 includes a miniature tungsten halogen light bulb. This light source may be used for either monochrome or color displays. The monochrome display simply directs the light from the bulb into the spatial light modulator as described above. However, for a color version, a conventional and readily providable FLC color filter (not shown), such as
10 Displaytech's RGB FASTfilter color filter, is positioned within the illumination arrangement 16 such that it filters the light produced by the tungsten halogen bulb allowing light of a restricted range of wavelengths to pass into the spatial light modulator at any given time. The FLC color filter is designed to selectively filter the
15 times. As was briefly described above and as will be described in more detail hereinafter, this light of various colors is modulated by the spatial light modulator to produce a color image for the viewer.

Referring now to Figures 3A-C, the FLC/VLSI spatial light modulator will be described in detail. In accordance with the present invention, FLC/VLSI spatial
20 light modulator 12 includes a thin layer of ferroelectric liquid crystal (FLC) 38 confined between a silicon VLSI circuitry backplane 40 and a glass window 42 coated on its inner side with a transparent electrode layer of indium-tin oxide (ITO) 44. VLSI backplane 40 includes an array of aluminum pads, one of which is indicated at 46, positioned on the upper surface of VLSI backplane 40. Aluminum pads 46 each have a reflective top
25 surface 48, best shown in Figure 3C, which is designed to reflect light directed into the spatial light modulator back out of the spatial light modulator. Each of the aluminum pads 46 making up the array of aluminum pads also acts as an electrode controlled by data writing arrangement 14 as mentioned above. These aluminum pad electrodes 46 and ITO electrode 44 positioned on the opposite side of FLC layer 38 are used to form
30 electric fields through FLC layer 38 and divide FLC layer 38 into individually controllable FLC pixels which correspond to the positions of aluminum pads 46.

In the presently preferred embodiment, FLC layer 38 is approximately one micron thick. This extremely thin FLC layer is significant because it is one of the

factors which allows the pixels of the present invention to be made smaller than has been previously possible. As was described above, aluminum pad electrode 46 is spaced apart from overall ITO electrode 44 by FLC layer 38 which as just stated is approximately one micron thick. This narrow spacing allows the aluminum pad electrodes 46 to produce
5 a well defined FLC pixel which does not spill over into adjacent pixels even when the light modulating area of the pixel is 10 microns or less in lateral extent. For example, in the case of square pixels (i.e. square pad electrodes 46) the pixels can be 10 microns or less on a side. Other liquid crystal materials, such as nematic liquid crystal for example, are not typically able to be used in layers this thin and therefore place larger
10 restrictions on the size of the pixel that can be produced.

Referring now to Figure 4 which illustrates a circuit schematic of a three by three array of pixels, the circuitry of a preferred embodiment of the VLSI backplane 40 will be described in detail. As shown in Figure 4 and in accordance with the present invention, each pixel comprises a subcircuit or cell including only one active component,
15 specifically an FET pass transistor 50 individually addressable by a word line 52 and a bit line 54, a charge storage capacitor 56, and a corresponding reflective aluminum pad 46. These components form a one bit storage register within each pixel. Data writing arrangement 14 writes the pixels one row at a time using word and bit lines 52 and 54. Also, in accordance with the present invention and because each pixel subcircuit uses
20 but one transistor, data writing arrangement 14, using a conventional row driver, by necessity writes each pixel row without the use of a data storage buffer of any kind within any of the pixel subcircuits since buffer circuitry would require additional active components within each of the pixel subcircuits. Thus, as soon as each row of pixels is written, its FLC layer begins to respond, so that if the rows of pixels are written one
25 by one the pixel rows' corresponding FLC responses will be unavoidably staggered in time. As will be described in more detail hereinafter, the novel data ordering schemes of the present invention fully compensate for this unavoidable staggering with respect to time of the FLC response. This is to be contrasted with a fully buffered approach in which each pixel subcircuit includes at least a second active component serving as a
30 buffer. In the fully buffered case, all of the pixels in the display can be activated at once eliminating any delays; however, the pixel subcircuits are more complex.

Because the above described one-transistor subcircuit is so simple and compact and because of the thinness of the FLC layer described above, the spatial light

modulator of the present invention allows for producing a pixel smaller than what has previously been possible. Therefore, this allows for producing an overall display smaller than what has been previously possible and a display with greater resolution. For example, using currently available 0.6 micron standard design rules for CMOS processing, a pixel pitch of about 5 microns may be achieved. This would result in a 1280 by 1024 pixel array having display dimensions of approximately 6 millimeters by 5 millimeters.

Referring back to Figure 3A-C, the operation of a single pixel will be described in detail, although the other pixels are independently operated in a similar manner. In one of the presently preferred embodiments, overall ITO electrode 44 is held at a constant 2.5 volts. Data writing arrangement 14 writes image data to each pixel by establishing the ON/OFF state of that pixel. This is done by establishing the voltage of the charge-storage capacitor within each pixel described above at either 0 and 5 volts. These two voltages provide oppositely directed electric fields between aluminum pad electrode 46, which defines each pixel, and ITO electrode 44, and therefore across FLC layer 38. Reversing the electric field formed through FLC layer 38 causes the FLC layer to reorient its molecular structure, thereby changing the light transmitting characteristics of the FLC layer. When the pixel is in the OFF state, the molecular structure of FLC layer 38 is in a first orientation. Any light directed into the pixel is reflected back out by aluminum pad 46. Since the FLC layer is oriented in this first position it does not effect the polarization of the light and as described above, the light is directed away from the viewing lens by polarizer/analyzer 24 presenting a dark pixel to the viewer. When the pixel is in the ON state, the molecular structure of FLC layer 38 is reoriented such that the polarization of any light reflected out of that pixel is changed, or in this particular embodiment the light polarization is rotated 90°. Since the light now has an orthogonal polarization, it is allowed to pass through polarizer/analyzer 24 into the viewing lens presenting a bright pixel to the viewer. By controlling each of the pixels in this way the overall array of pixels may be used to form an image consisting of bright or dark pixels at any given time.

Although only one embodiment of the FLC/VLSI spatial light modulator has been described in detail, it should be understood that the components of the modulator may be embodied in many other specific forms without departing from the spirit or scope of the invention. For instance, the VLSI backplane 40 has been described

as including an array of aluminum pads 46. Although aluminum pads 46 have been pictured as being square in shape, it should be understood that these pads may take on a wide variety of shapes such as but not limited to circular, octagonal, rectangular or any irregular shape depending on the requirements of the system. Also, it should be understood that the pads, although described as being aluminum, may be made from other electrically conductive and reflective metals. This, of course, assumes that the overall system is a reflective system as illustrated in Figure 1. Alternatively, the present invention contemplates a transmissive system in which case the VLSI active matrix backplane is realized using silicon on sapphire technology which allows for light to be transmitted therethrough rather than reflected off of its components. In this case, the light source and its various optical components would be modified to accommodate the transmissive mode as opposed to the reflective mode.

Furthermore, the present invention is not limited to the specific voltages used in the above described preferred embodiment, so long as suitable voltages are selected to create the desired electric fields. Other embodiments of the present invention may incorporate additional circuit elements in the VLSI circuitry making up each pixel, or, between the data writing arrangement and the pixels. Other embodiments may also make use of an FLC layer having a thickness other than approximately one micron thick. All of these various embodiments would still fall within the spirit and scope of the present invention. Although the data writing arrangement has been described as writing the pixels row by row, this is not a requirement of the present invention. Instead, the pixels may be written in any desired sequence whether that is individual pixel by pixel, column by column, or some other sequence of specific groups of pixels.

Now that the configuration of the display system has been described in detail, the operation of the display system using data writing arrangement 14 will be described. As will be seen hereinafter, because of the fast switching characteristics of ferroelectric liquid crystal, FLC/VLSI spatial light modulator 12 has the ability to obtain a broad range of gray scale by using a variety of specific data ordering schemes designed in accordance with the present invention. Each of these data ordering schemes contemplates first establishing a particular frame time (which may be the same or vary from frame to frame) for the display system. In fact, in the case of printers, the overall operation of the system may take place during a single frame time. Frame rates of 45 frames per second or higher (i.e. video frame rates) are contemplated by the present

invention which means that, for example, for a frame rate of 60 frames per second, each frame would be approximately 16.7 milliseconds long. As will be described in more detail hereinafter, each of these frames is then divided into a plurality of subframes for purposes of obtaining gray scale in the case of a monochromatic system or subframes
5 for purposes of obtaining color and sub-subframes for purposes of obtaining color gray scale in the case of a color system.

In accordance with one data ordering scheme which will be described in more detail later and which will be hereinafter referred to as the time domain scheme, the gray scale of any given pixel is obtained by controlling the length of time that pixel
10 is in the ON state during each frame. This is done by dividing the frames into subframes while maintaining the light directed into the modulator at a fixed brightness and by either turning ON or OFF the particular pixel during certain subframes of the frame such that the cumulative time in which the pixel is ON during the frame is proportional to the desired gray scale for that pixel. This is done for every pixel of the
15 array for every frame.

Alternatively, in accordance with a second data ordering scheme which will later be described in more detail and which will hereinafter be referred to as the brightness domain scheme, the gray scale of any given pixel is obtained by dividing each frame into subframes and by turning ON or OFF the particular pixel during certain
20 subframes of the frame while at the same time changing the brightness of light directed into the spatial light modulator for each of the different subframes. The cumulative time in which the pixel is ON, in combination with the brightness of the light during each subframe, is proportional to the desired gray scale for that pixel.

A third scheme hereinafter referred to as the hybrid scheme is a
25 combination of the time domain scheme and brightness domain scheme. In all three of these schemes, because of the frame rate or speed at which the display presents frames to a viewer, the viewer's eye perceives the time integral of brightness of each pixel for each frame, thereby perceiving the desired gray scale image. Similarly, for a printer, a light sensitive print medium also integrates the optical energy for each pixel to yield
30 a gray level which varies with the time integral of the brightness of each pixel.

Now, the various schemes for producing an active matrix display using data ordering techniques to obtain gray scale in accordance with the present invention and preferably at video rates will be described in detail. As was briefly described

above, these schemes are generally categorized as the time domain scheme, the brightness domain scheme and the hybrid scheme. For each of these schemes, a desired frame rate is first established. This frame rate is typically set at a rate which produces smooth motion to the viewer's eye, for example, 60 frames per second which is a common frame rate used for video applications. Although a frame rate of 60 frames per second is used as an example, it should be understood that the present invention is not limited to this specific frame rate.

Referring to Figure 5, a simple example of the time domain scheme for obtaining gray scale for the display will first be described for an individual pixel. Figure 5 is a graph illustrating the ON/OFF state of an individual pixel relative to time for two consecutive frames. As described above for the time domain scheme, the light directed into the spatial light modulator is held at a constant brightness and the perceived gray scale of each pixel increases with the cumulative time during each complete frame in which that pixel is in the ON state.

As shown in Figure 5 for the time domain scheme and in accordance with the present invention, frames F1 and F2 are divided into subframes having different lengths of time. In this example, frames F1 and F2 are divided into three subframes with a first subframe S1 being equal to $1/7$ of the length of overall frame F1, a second subframe S2 twice as long as the first subframe S1 or $2/7$ of overall frame F1, and a third subframe S3 twice as long as the second subframe S2 or $4/7$ of overall frame F1. For each of these subframes, data writing arrangement 14 writes each pixel using a single binary bit to establish whether the pixel is in the ON or OFF state during each of the subframes. Only one bit is required to determine the state of the pixel. Thus, for example, writing arrangement 14 is able to provide digital data commands in the form of zeros and ones to control the ON/OFF state of each pixel during any given subframe. In this example, a zero (0) is used for turning a pixel OFF from an ON state or maintaining the pixel in an OFF state and a one (1) is used for turning a pixel ON from an OFF state or maintaining the pixel in an ON state.

By dividing the frame as described above, that is into three subframes of the different durations noted, eight levels of gray scale having equal changes in gray scale from level to level are achieved. These gray scale levels range from level 0, which corresponds to the pixel being in the OFF state throughout all three subframes of the frame, to level 7, which corresponds to the pixel being in the ON state throughout

all three subframes of the frame. Any of the gray scale levels between level 0 and level 7 may be obtained by turning ON the pixel during the appropriate subframes.

As mentioned above, gray scale level 0 is obtained by turning OFF the pixel for all three subframes S1, S2, and S3 of the frame causing the pixel to be as dark as possible for that frame. This is a result of data commands from data writing arrangement 14 of zero (0) for each of the subframes S1, S2, and S3 which may be represented as a series of binary bits 0-0-0 with the first bit of the series corresponding to subframe S1, the second to subframe S2, and the third to subframe S3. Gray scale level 1 is obtained by turning ON the pixel during subframe S1 which is 1/7 of the overall length of the frame time, and turning it OFF for subframes S2 and S3. This causes the pixel to be ON for 1/7 of the frame and results in a pixel 1/7 as bright as would be the case if the pixel were left ON throughout the frame (i.e. gray scale level 7). Therefore, gray scale level 1 corresponds to data commands of one (1) for subframe S1, and zero (0) for subframe S2 and S3 which may be represented as a series of binary bits 1-0-0. Gray scale level 2 is obtained by turning ON the pixel during only subframe S2 which is 2/7 of the length of the frame. This causes the pixel to be ON for 2/7 of the overall frame resulting in a pixel 2/7 as bright as possible. Gray scale level 2 corresponds to data command 0-1-0. Using this three bit data command format, gray scale level 3 corresponds to data command 1-1-0, level 4 corresponds to command 0-0-1, level 5 to 1-0-1, level 6 to 0-1-1, and gray scale level 7 corresponds to data command 1-1-1. Accordingly, for each successive gray scale level, the pixel is ON for an additional 1/7 of the overall time of the frame and therefore results in a pixel brighter by 1/7 of the maximum brightness than the previous gray scale level. Thus, including gray scale level 0 which corresponds to the pixel being OFF for all three subframes, eight levels of gray scale are achieved with each level having equal changes in gray scale from level to level which is hereinafter referred to as linear gray scale.

Still referring to Figure 5, one specific example of the time domain scheme will be described in detail. In this example, the pixel's state is switched for each of the subframes for illustrative purposes. The first frame corresponds to a gray scale level of 5 (1-0-1) and the second frame corresponds to a gray scale level of 2 (0-1-0). As shown in Figure 5 at curve 58 which represents the actual switching time of the FLC material, when the pixel is switched to the ON state from its OFF state, the pixel takes a certain amount of time to respond. A typical amount of time for the FLC material

used in the present invention to fully respond is approximately 100 microseconds. Also shown in Figure 5 at curve 60, the pixel takes approximately the same amount of time to respond when it is switched to the OFF state from the ON state.

As the FLC material responds, it begins to change the polarization of the light directed into that pixel which in turn causes the amount of light directed to the viewer to change in proportion to the change in polarization. Since the FLC material responds in a similar way both when turned ON and OFF, the extra time the pixel is partially ON when it is turned OFF makes up for the time it is partially OFF when it is turned ON. In other words, the fall time of curve 60 in Figure 5 which corresponds to the time the pixel takes to turn fully OFF makes up for the rise time of curve 58 which corresponds to the time the pixel takes to turn fully ON. Since the fall time of curve 60 exactly makes up for the rise time of curve 58, the actual time the pixel is ON is the same as if the pixel were able to respond instantaneously when switched, so long as one subframe follows another without a change in light source brightness or color. Under this condition, the rise time tail 58A (shaded area above curve 58 in Figure 5) or the fall time tail 60A (shaded area below curve 60 in Figure 5) during any given subframe is combined with either the fall time of the following subframe or the rise time of the proceeding subframe to fill one complete subframe regardless of response time. For this example, this means that even with the rise and fall time tails 58A and 60A which indicate that the pixel is in the process of turning partially ON or OFF, when combining the subframes, the cumulative amount of light which the FLC material of the pixel changes in polarity during any given subframe is exactly 1/7, 2/7, 3/7, or so on up to 7/7 of the polarized light directed into the pixel throughout the overall frame. This is the case for each of the consecutive gray scale levels which causes a corresponding increase of exactly 1/7 of the polarized light directed into the pixel to be changed in polarity. Therefore, the viewer's eye receives the eight levels of gray scale with equal incremental changes in gray scale from level to level thereby maintaining the linearity of the gray scale.

Also, as shown in Figure 5, these tails 58A and 60A occur between frames as well as subframes. This again is necessary in order for maintaining the linearity of gray scale in the same manner just described with respect to subframes, but again so long as one frame follows another without changes in light source brightness or color. As long as the pixel is written at the proper time (i.e. at the beginning of the

subframe in which the ON/OFF state is to be changed) and the light directed into the pixel is held constant in intensity without a change in color, the proper levels of gray scale will be maintained both from subframe to subframe and from frame to frame irrespective of the presence of tails 58A and 60A. However, as will be described in more detail hereinafter, if the light is varied in any way (either in terms of brightness or color) from subframe to subframe or frame to frame, the response time of the FLC material indicated by curves 58 and 60 of Figure 5 resulting in tails 58A and 60A would pose a problem for maintaining the proper incremental change of gray scale from subframe to subframe and frame to frame.

Although the above example describes dividing the frame into three subframes in order to obtain eight levels of gray scale, it should be understood that the present invention would apply regardless of the number of subframes that the frame is divided into, so long as the shortest subframe allows the FLC material to fully respond during that subframe. By adding subframes, each of which is twice as long as the proceeding subframe, the number of gray scale levels is increased by a factor of two for each subframe added. Therefore, four subframes would provide 16 gray scale levels (0-15), five subframes would provide 32 levels (0-31), and so on up to as many or more than eight subframes which would provide 256 levels of gray scale (0-255). However, using a frame rate of 60 frames per second and dividing the frames into eight subframes of different lengths which successively double in duration as described above, the shortest subframe would be approximately 65 microseconds long. This would not be a long enough amount of time to allow the FLC material to fully respond if the FLC material required 100 microseconds to fully respond as mentioned above. However, it should be understood that FLC materials having a faster switching speed may be used while remaining within the scope of the present invention thereby solving this time problem. Alternatively, this problem may be solved by using the brightness domain scheme for at least some of the subframes as will be described in detail hereinafter. On the other hand, operating at 128 gray scale levels (seven subframes), the shortest subframe would be about 130 microseconds long, providing plenty of time for the 100 microsecond response time of the FLC material.

Referring now to Figure 6, the example given for a single pixel operating with the time domain scheme to obtain gray scale will be expanded to illustrate the operation of the three pixel by three pixel array shown in Figure 4. The graph in Figure

6 illustrates the ON/OFF state of each of the three rows of pixels relative to time with the rows of pixels being arranged in order, row by row, from top to bottom with pixel R1 being the top row of pixels and row R3 being the bottom row of pixels. In this example, for illustrative purposes, each of the individual pixels within each row
5 simultaneously receives the same data and therefore has a graph identical to the graph described above in Figure 5 with each of frames F1 and F2 being divided into subframes S1, S2, and S3. However, as especially illustrated in Figure 6, the beginning of each of the pixel graphs corresponding to each of the rows of pixels R1 through R3 are shifted timewise a predetermined amount successively from pixel row R1 to pixel row
10 R2 and from pixel row R2 to pixel row R3. This is because, as described above, the data writing arrangement sequentially writes each pixel row without the use of a buffer in any of the pixel subcircuits, and therefore takes a certain amount of time to write each pixel row.

In the preferred embodiment, for the overall display described herein to
15 function properly, it is important that all of the pixel rows in the array are written within the time of any single subframe. This is necessary in order to allow the first pixel row written in any given subframe to be written again at the beginning of the next consecutive subframe. As the number of pixel rows making up the display is increased, the overall time required to write all of the pixel rows making up the array also
20 increases. However, as mentioned above, even for the shortest subframe S1, all of the pixel rows of the array must be written during the time available for that subframe S1 so that the first pixel row may be again written for the next subframe S2 at the proper time. When the number of gray scale levels is increased as described above, this amount of time is cut in half for each subframe added. Also, as described above, the
25 shortest subframe must be long enough for the FLC material to fully respond to any change in ON/OFF state. Therefore, the shortest subframe S1 must be sufficiently long (i) to allow the data writing arrangement to write all of the pixel rows in the array and (ii) to allow each pixel (actually the FLC) to fully respond (either entirely turn ON or OFF). Thus, in the case where it takes 100 microseconds for a pixel to respond, the
30 shortest subframe S1 can be no shorter than that even if the pixel rows could be written at a faster rate.

Referring now to Figure 7, the brightness domain scheme for obtaining gray scale will be described in detail. As mentioned above for the brightness domain

scheme, the brightness of the light directed into the spatial light modulator is changed for each subframe and the gray scale of each pixel is proportional to the cumulative time during a complete frame in which that pixel is in the ON state in combination with the brightness of the light during each of those subframes.

5 Figure 7 illustrates the ON/OFF state of an individual pixel relative to time for two consecutive frames using a first particular brightness domain scheme for writing the pixels. In this scheme, each of the frames F1' and F2' are divided into subframes S1', S2', and S3' of equal length. However as will be described in more detail hereinafter, because (i) it takes time for the brightness of the light to be changed
10 after each subframe, (ii) it takes time for the FLC material to respond to a change in state and (iii) it takes time to write each of the pixels in the array; a blackout subframe SB is used after each subframe S1', S2', and S3' in accordance with the present invention to maintain the proper levels of gray scale from subframe to subframe. In this case, rise time tails and fall time tails cannot be combined to compensate for one
15 another as in the time domain case since source brightness changes from subframe to subframe. As shown in Figure 7, blackout subframe SB provides enough time ΔR for the pixel to be switched to the OFF state and to fully respond along with providing an additional amount of time ΔB for changing the brightness of the light. In this embodiment, all of the pixels must be switched OFF and must have fully responded prior
20 to changing the brightness of the light.

The blackout subframes SB are important in order to maintain the proper gray scale levels for this particular brightness domain scheme of writing the pixels. This is because, as described above for the time domain scheme, the pixel has a certain response time for switching its ON/OFF state represented by curves 58 and 60 in Figure
25 7. As was described in detail for the time domain approach, the fall time tail 60A (curve 60) makes up for the rise time tail 58A (curve 58) such that the cumulative amount of time the pixel is ON is the proper amount of time to maintain an even change of gray scale from level to level. However, the light directed into the pixel must be held constant (and of the same color) throughout both the rise time (curve 58) and the fall
30 time (curve 60) for each subframe or the total amount of light of which the polarization is changed by the FLC material will not be the exact amount necessary to maintain an incrementally even changing gray scale from gray scale level to gray scale level. Therefore, blackout subframes SB are provided after each subframe S1', S2', and S3'

in order to provide enough time, ΔR , for the pixel to fully turn OFF prior to changing the brightness of light directed into the pixel. Also, since a certain amount of time may be required in order to change the brightness of light directed into the pixel, an additional amount of time ΔB may be provided within subframe SB which provides
5 enough time for the brightness of light directed into the pixel to be changed. As mentioned above, in this embodiment, all of the pixels must be switched OFF and must have fully responded prior to changing the brightness of the light.

In the example illustrated in Figure 7, each of the frames F1' and F2' are divided into three subframes S1', S2', and S3' each being followed by a blackout
10 subframe SB as described above and each being of an equal length of time. However, contrary to the time domain scheme, for the brightness domain scheme of obtaining gray scale, the brightness of light directed into the pixel is changed for each subframe S1', S2', and S3'. During subframe S3', the maximum brightness of light available, represented by brightness B3 in Figure 7, is directed into the pixel. During subframe
15 S2', the brightness of light directed into the pixel is reduced to half the brightness of light which was directed into the pixel during subframe S3'. This brightness is represented by brightness B2 in Figure 7. And finally, during subframe S1', the brightness of light directed into the pixel, represented by brightness B1, is reduced to half the brightness of B2. Therefore, the brightness of the light directed into the pixel
20 is increase by a factor of two for each consecutive subframe S1', S2', and S3' within the frames F1' and F2'.

Dividing the frame into three subframes as described above provides eight levels of gray scale (level 0 through level 7) in a way similar to what was described above for the time domain scheme except that the brightness of the light is
25 increased by a factor of two for each of the subframes rather than increasing the length of time for each successive subframe. As was described in detail for the time domain scheme, data writing arrangement 14 controls each pixel using a binary bit (either a zero for OFF or a one for ON) to establish the ON/OFF state of each pixel for each subframe. However, when using the data command format described for the time
30 domain scheme, the brightness domain scheme requires three additional bits corresponding to the blackout subframes and therefore requires a series of six bits to fully control each frame for the example shown in Figure 7.

Specifically, as shown in Figure 7, gray scale level 7 is obtained by

turning ON the pixel for all three subframes S1', S2', and S3' of the frame causing the pixel to be as bright as possible for that frame. This is a result of data commands from data writing arrangement 14 of one (1) for each of the subframes S1', S2', and S3' and zeros (0) for each of the blackout subframes SB following subframes S1', S2', and S3' which may be represented by a series of six binary bits 1-0-1-0-1-0 with the first bit corresponding to subframe S1', the second bit corresponding to the blackout subframe SB following S1' and so on though the final blackout subframe SB at the end of the frame. Using this six bit data command format, gray scale level 6 corresponds to command 0-0-1-0-1-0, level 5 to 1-0-0-0-1-0, level 4 to 0-0-0-0-1-0, level 3 to 1-0-1-0-0-0, level 2 to 0-0-1-0-0-0, level 1 to 1-0-0-0-0-0, and finally gray scale level 0 corresponds to data command 0-0-0-0-0-0. Since brightness B2 of subframe S2' is twice as bright as brightness B1 of subframe S1' and subframe S1' and S2' are of equal length of time, the total amount of light available in subframe S2' is twice that available in subframe S1'. Also, since brightness B3 of subframe S3' is four times as bright as B1 and subframe S1' and S3' are of equal length of time, the total amount of available light in subframe S3' is four times that available in S1'. Therefore, the total amount of light available in subframe S1' is 1/7, S2' is 2/7 and S3' is 4/7 of the total amount of the overall light available in subframes S1', S2', and S3'. By turning ON and OFF the pixel as described immediately above, eight gray scale levels (0-7) are obtained with each successive level having a brightness 1/7 of the maximum brightness brighter than the previous level.

As mentioned above for the time domain scheme, although the above example describes dividing the frame into three subframes each followed by a blackout subframe to obtain eight levels of gray scale, it should be understood that the present invention would apply regardless of the number of subframes that the frame is divided into, so long as the length of the subframes, timewise, allows the FLC material to fully respond during the subframes. By adding subframes with a corresponding blackout subframe, each of which has light of a brightness twice as bright as the proceeding subframe, the number of gray scale levels is increased by a factor of two for each subframe added. Therefore, four subframes with their corresponding blackout subframes would provide 16 gray scale levels (0-15), five subframes would provide 32 levels (0-31), and so on up to as many or more than eight subframes with their corresponding blackout subframes which would provide 256 levels of gray scale (0-255).

Referring now to Figure 8, the example given for a single pixel operating using the above described brightness domain scheme to obtain gray scale will be expanded to illustrate the operation of the three pixel by three pixel array shown in Figure 4. The graph in Figure 8 illustrates the ON/OFF state of each of the three rows of pixels relative to time with the rows of pixels being arranged in order, row by row, from top to bottom with pixel row R1 being the top row of pixels and pixel row R3 being the bottom pixel row. In this example, for illustrative purposes, each of the individual pixels within each pixel row simultaneously receives the same data and therefore has a graph identical to the graph described above in Figure 7 with frame F1' being divided into subframes S1', S2', and S3' each with their corresponding blackout subframe SB. However, the beginning of each of the pixel graphs corresponding to pixel rows R1 through R3 are shifted timewise a predetermined amount successively from pixel row R1 to pixel row R2 and from pixel row R2 to pixel row R3. This is because, as described above, the data writing arrangement sequentially writes each pixel row without the use of any buffer circuitry within the individual pixel subcircuits, and therefore takes a certain amount of time to write each pixel row.

As described in detail above for the time domain scheme, for the overall display described herein to function properly, all of the pixels in the array must be written within the time of any single subframe. This is necessary in order to allow the first pixel row written in any given subframe to be written again at the beginning of the next consecutive subframe. However, for the brightness domain scheme, as shown in Figure 8, each subframe is followed by a blackout subframe SB made up of three portions; a first portion $\Delta R1$ which is long enough for all pixel rows in the array to be switched OFF by data writing arrangement 14 (writing time for all pixel rows), a second portion $\Delta R2$ which is long enough for the pixels in the array to fully turn OFF (individual pixel FLC response time), and a third portion ΔB which is long enough for the brightness of light directed into each pixel to be fully changed to the appropriate brightness for the next subframe if the light is not able to switch brightness instantaneously. Therefore, for example, using the same FLC response time ($\Delta R2$) of 100 microseconds used in previous examples and also using 100 microseconds as the time ($\Delta R1$) needed for writing all of the pixel rows in the array for each subframe, the approximate minimum total length of each subframe and its corresponding black out subframe SB would be; 100 microseconds (the FLC response time) for the subframe

itself, plus blackout subframe time SB consisting of 100 microseconds (the time allowed for writing all the pixel rows) for the $\Delta R1$ portion of blackout subframe SB, plus 100 microseconds (the FLC response time) for the $\Delta R2$ portion of blackout subframe SB, plus the time ΔB it takes to switch the brightness of the light (if necessary). Thus the
5 minimum length of a brightness domain subframe and its corresponding blackout subframe, for this example, would be approximately 300 microseconds plus the ΔB switching time (if necessary).

Because each of the subframes combined with their corresponding blackout subframes are of an equal length of time, the brightness domain scheme has the
10 advantage of not being restricted by the length, timewise, of the shortest subframe as was described above for the time domain scheme. For example, using the same frame rate of 60 frames per second (giving a frame length of 16,666 microseconds), the same response time for the FLC material of 100 microseconds, and the same writing time of 100 microseconds as was used in the examples given for the time domain scheme, the
15 minimum length of each subframe including its corresponding blackout subframe for the brightness domain scheme would be approximately 300 microseconds plus a time ΔB as described immediately above. This would allow enough time within each frame for approximately fifty five subframes, as compared to a limit of seven subframes using the time domain approach as mentioned above in the time domain example. Since the
20 number of gray scale levels is increased by a factor of two for each subframe added, fifty five subframes would provide far more gray scale levels than would be practical. Therefore, since fifty five subframes are not necessary, each of the subframes which are employed may be substantially longer than the minimum length described above. This improves the effective use of the light source and the overall brightness of the display
25 by allowing the pixels to be in their fully ON state for a longer period of time during any given subframe. For example, to provide 256 gray scale levels 8 subframes are required. Dividing the frames into subframes of equal length, each subframe and its associated blackout subframe would be approximately 2000 microseconds. Subtracting the 200 microseconds for the blackout subframe plus the time ΔB required to switch the
30 brightness of the light (if necessary), allows the subframe itself to be approximately 1800 microseconds. Since the FLC material takes 100 microseconds to fully respond, in this example, a pixel in the ON state for a particular subframe would be fully ON for approximately 1700 microseconds.

Although the embodiment described utilizes a blackout subframe in a spatial light modulator using a FLC layer as the modulating material, it should be understood that the use of a blackout subframe in a brightness domain scheme is not limited to being used with this modulating material. In fact this use of a blackout subframe within a brightness domain scheme would equally apply regardless of the modulating medium used including nematic liquid crystal or other modulating materials.

In the brightness domain scheme, for each additional subframe and its corresponding blackout subframe used in the brightness scheme, the brightness of light directed into the pixel must be increased by a factor of two. Therefore, as the number of gray scale levels is increased, the effective usage of any given light source is reduced since the light can only be used at its brightest possible level for one subframe and for the other subframes it must be dimmed by a factor of two for each additional subframe, thereby compromising its efficiency significantly. Alternatively, in the time domain approach, the light source may be used at its maximum brightness throughout the operation resulting in very efficient use of the light source, however, the shortest subframe is restricted to a minimum duration which restricts the number of achievable gray scale levels. This problem of using the light source inefficiently in the brightness domain approach and the problem of being limited by the shortest subframe in the time domain approach can be solved in accordance with the present invention by using a combination of the time domain scheme and the brightness domain scheme as will be described in detail hereinafter.

Alternatively, Figure 9 illustrates the ON/OFF state of an individual pixel relative to time for two consecutive frames using a second particular brightness domain scheme for writing the pixels. In this scheme, each of the frames F1' and F2' are divided into subframes S1', S2', and S3' of equal length as described above for the first brightness domain scheme. However, as will be described in more detail hereinafter, in this second brightness domain scheme, the light source is turned OFF during certain portions of each subframe to allow all of the pixel rows to be written and fully respond prior to turning the light ON. This eliminates the need for the above described blackout subframes which instead uses the pixels themselves to create the blackout. Specifically, in the previous described embodiment, the blackout was created by turning all of the pixels OFF. In this alternative embodiment, the light source is turned OFF to create a blackout. Also, in this case, since the light is OFF while the pixel rows are being

written and fully responding, the rise time tails and fall time tails occur during the blackout time when the light is OFF and therefore these tails do not create any problem for maintaining the linearity of the gray scale.

In the example illustrated in Figure 9, each of the frames F1' and F2' are
5 divided into three subframes S1', S2', and S3' each being of an equal length of time. As was described above for the first brightness domain scheme, the brightness of light directed into the pixel is changed for each subframe S1', S2', and S3'. During a specific portion B3 of subframe S3' after the pixel has been written and fully responded, the maximum brightness of light available, represented by brightness B3 in Figure 9, is
10 directed into the pixel. During a specific portion B2 of subframe S2' after the pixel has been written and fully responded, light having a brightness half the brightness of light which was directed into the pixel during subframe S3' is directed into the pixel. This brightness is represented by brightness B2 in Figure 9 and this brightness is directed into the pixel for the same amount of time as B3. And finally, during a specific portion B1
15 of subframe S1' after the pixel has been written and fully responded, light having a brightness half the brightness of light which was directed into the pixel during subframe S2' is directed into the pixel. This brightness is represented by brightness B1 in Figure 9 and is directed into the pixel for the same amount of time as B3 and B2. Therefore, the brightness of the light directed into the pixel is increase by a factor of two for each
20 consecutive subframe S1', S2', and S3' within the frames F1' and F2'. In addition, the light is turned off entirely in each subframe before the pixel is written for the succeeding subframe thereby eliminating all possible dependence of the gray scale level on the response times 58 and 60 of the FLC.

Dividing the frame into three subframes as described above provides eight
25 levels of gray scale (level 0 through level 7) in a way similar to what was described above for the first brightness domain scheme except that the blackout subframes are not required. As was described in detail for the time domain scheme, data writing arrangement 14 controls each pixel using a binary bit (either a zero for OFF or a one for ON) to establish the ON/OFF state of each pixel for each subframe. Specifically,
30 as shown in Figure 9 which illustrates gray scale level 5, gray scale level 5 is obtained by turning ON the pixel for subframes S1' and S3' and turning the pixel OFF for subframe S2' of the frame. This is a result of data commands from data writing arrangement 14 of one (1) for each of the subframes S1' and S3' and a zero (0) for

subframe S2' which may be represented by a series of three binary bits 1-0-1 with the first bit corresponding to subframe S1', the second bit corresponding to the subframe S2', and the third bit corresponding to the subframe S3'. Using this three bit data command format, gray scale level 7 corresponds to command 1-1-1, level 6 to 0-1-1, level 4 to 0-0-1, level 3 to 1-1-0, level 2 to 0-1-0, level 1 to 1-0-0, and finally gray scale level 0 corresponds to data command 0-0-0. Since brightness B2 during subframe S2' is twice as bright as brightness B1 during subframe S1' and brightness B2 and B1 are ON for an equal length of time, the total amount of light available in subframe S2' is twice that available in subframe S1'. Also, since brightness B3 during subframe S3' is four times as bright as B1 and brightness B3 and B2 are ON for an equal length of time, the total amount of available light in subframe S3' is four times that available in S1'. Therefore, the total amount of light available in subframe S1' is 1/7, S2' is 2/7 and S3' is 4/7 of the total amount of the overall light available in subframes S1', S2', and S3'. By turning ON and OFF the pixel and turning ON and OFF the light as described immediately above, eight gray scale levels (0-7) are obtained with each successive level having a brightness 1/7 of the maximum brightness brighter than the previous level.

As mentioned above for the time domain scheme, although the above example describes dividing the frame into three subframes to obtain eight levels of gray scale, it should be understood that the present invention would apply regardless of the number of subframes that the frame is divided into, so long as the length of the subframes, timewise, allows (i) all of the rows of pixels to be written, (ii) the FLC material to fully respond, and after all of the rows have been written and fully responded allows (iii) an equal amount of time during each subframe for the light to be directed into the pixels at the proper brightness. By adding subframes, each of which has light of a brightness twice as bright as the proceeding subframe, the number of gray scale levels is increased by a factor of two for each subframe added. Therefore, four subframes would provide 16 gray scale levels (0-15), five subframes would provide 32 levels (0-31), and so on up to as many or more than eight subframes which would provide 256 levels of gray scale (0-255).

Referring now to Figure 10, the example given for a single pixel operating using the above described second brightness domain scheme to obtain gray scale will be expanded to illustrate the operation of the three pixel by three pixel array shown in Figure 4. The graph in Figure 10 illustrates the ON/OFF state of each of the three

rows of pixels relative to time with the rows of pixels being arranged in order, row by row, from top to bottom with pixel row R1 being the top row of pixels and pixel row R3 being the bottom pixel row. In this example, for illustrative purposes, each of the individual pixels within each pixel row simultaneously receives the same data and
5 therefore has a graph identical to the graph described above in Figure 9 with frame F1' being divided into subframes S1', S2', and S3'. However, the beginning of each of the pixel graphs corresponding to pixel rows R1 through R3 are shifted timewise a predetermined amount successively from pixel row R1 to pixel row R2 and from pixel row R2 to pixel row R3. This is because, as described above, the data writing
10 arrangement sequentially writes each pixel row without the use of any buffer circuitry within the individual pixel subcircuits, and therefore takes a certain amount of time to write each pixel row.

As described in detail above for the time domain scheme, for the overall display described herein to function properly, all of the pixels in the array must be
15 written within the time of any single subframe. Also, for this embodiment of the brightness domain scheme, each subframe must be long enough, after all of the pixels have been written and fully responded, to direct the proper brightness of light into the pixels for the proper amount of time. As shown in Figure 10, each of the subframes S1', S2', and S3' are made up of three portions, a first portion $\Delta R1$ which is the amount
20 of time it takes to write all of the rows of pixels, a second portion $\Delta R2$ which is the amount of time it takes for the pixels to fully respond, and a third portion B1, B2, or B3 which is the proper length of time in which the proper brightness of light is directed into the pixel array as described above. As mentioned above, because in this second brightness domain scheme the light source is turned OFF during certain portions,
25 specifically times $\Delta R1$ and $\Delta R2$, of each subframe to allow all of the pixel rows to be written ($\Delta R1$) and fully respond ($\Delta R2$) prior to turning the light ON, the need for blackout subframes described for the first brightness domain scheme is eliminated. Also, in this case, since the light is OFF while the pixel rows are being written and fully responding, the rise time tails and fall time tails do not create any problem for
30 maintaining the fidelity of the gray scale as was the case for the time domain scheme and the first brightness domain scheme described above. For this version of the brightness domain scheme, it is assumed that the time needed to turn the light source on and off is negligible. If this is not the case for some light source, those skilled in the art will

readily recognize that the times B1, B2, and B3 may be made unequal so as to preserve the required geometric progression of the time integral of brightness which must hold for the subframes.

As mentioned above for the other brightness domain scheme, because each
5 of the subframes are of an equal length of time, the brightness domain scheme has the advantage of not being restricted by the length, timewise, of the shortest subframe as was described above for the time domain scheme. This allows for more subframes within a frame, therefore allowing for a greater number of gray scale levels. However, as also mentioned above, in the brightness domain scheme, for each additional subframe
10 used in the brightness scheme, the brightness of light directed into the pixel must be increased by a factor of two. Therefore, as the number of gray scale levels is increased, the effective usage of any given light source is reduced since the light can only be used at its brightest possible level for one subframe and for the other sub frames it must be dimmed by a factor of two for each additional subframe, thereby compromising its
15 efficiency significantly. Alternatively, in the time domain approach, the light source may be used at its maximum brightness throughout the operation resulting in very efficient use of the light source. This problem of using the light source inefficiently in the brightness domain approach and the problem of being limited by the shortest subframe in the time domain approach can be solved in accordance with the present
20 invention by using a combination of the time domain scheme and the brightness domain scheme as will be described in detail hereinafter.

Referring now to Figures 11 and 12, the hybrid scheme for obtaining gray scale will be described. As was explained above for the time domain scheme, the number of gray scale levels is limited by the time required for the FLC material to fully
25 respond to a change in state. In the hybrid scheme, the time domain scheme is typically used for as many subframes as possible until the size of the subframes, timewise, approach the length of time which is required for the FLC material to respond, or, the length of time required to write all of the pixel rows, which ever is longer. In the hybrid scheme, any desired remaining subframes, the number of which will depend on
30 the levels of gray scale desired, will have the same length of time as the smallest time domain subframe. However, during these latter subframes, the brightness of the light directed into the modulator will be successively reduced by a factor of two as was described above for the brightness domain scheme. Also, if the first described

brightness domain scheme is used, each time the brightness of the light is changed, a blackout subframe is positioned between the two subframes between which the brightness of light is to be changed. This combination of the time domain and brightness domain schemes preserves the linearity of the gray scale levels without requiring subframes
5 shorter than (i) the time required for the FLC material to change state or (ii) the time required to write all of the pixel rows in the array. It also utilizes the light source more efficiently by using the time domain scheme as much as possible which keeps the light source at its maximum output as much as possible.

Referring to Figure 11, one specific example of an individual pixel
10 controlled by data writing arrangement 14 using the hybrid scheme to obtain gray scale will be described in detail. In this example, thirty two levels (0-31) of gray scale are provided by dividing each frame FH into five subframes SH1 through SH5. Subframes SH1 and SH2 are controlled using the first described brightness domain scheme and therefore are of equal length and have differing brightness of light B1 and B2,
15 respectively, associated with them. Subframes SH3, SH4, and SH5 are controlled using the time domain scheme and therefore have a constant brightness B3 associated with them which is, in accordance with the hybrid scheme, twice as bright as B2 and four times as bright as B1. As described above for the time domain scheme, subframe SH5 is twice as long as SH4, which is twice as long as SH3, which in accordance with the
20 hybrid scheme is the same length as SH1 and SH2. Each time the brightness of the light is changed, that is after SH1, SH2, and SH5, an additional blackout subframe SB is inserted in accordance with the present invention and as was described in detail above for the first described brightness domain scheme.

With the above described arrangement of subframes, data writing
25 arrangement 14 is able to write the pixel using a binary bit (i.e. a zero or a one) for each of the five subframes and three blackout subframes making up overall frame FH. Since subframe SH1 is 1/4 as long as SH5 and B1 is 1/4 as bright as B3, the total light available during subframe SH1 is 1/16 of that available during SH5 and 1/31 of the total light available throughout frame FH. Since SH2 is 1/4 as long as SH5 and B2 is 1/2 as
30 bright as B3, the total light available during SH2 is 2/16 of that available during SH5 and 2/31 of the total light available throughout frame FH. Since SH3 is 1/4 as long as SH5 and has the same brightness, the total amount of light available during SH3 is 4/16 of that available during SH5 and 4/31 of the total light available throughout frame FH.

And finally, since SH4 is half as long as SH5, the light available during SH4 is 8/16 of that available during SH5 and 8/31 of the total light available throughout frame FH. Therefore, by controlling the ON/OFF state of the pixel during each of the subframes of frame FH, thirty two gray scale levels (0-31) having equal changes in gray scale from level to level are provided. For example, as shown in Figure 11, gray scale level 23 is obtained by having data writing arrangement 14 provide a series of eight binary bits 1-0-1-0-1-0-1-0 with the first bit corresponding to subframe SH1, the second bit to blackout subframe SB following SH1, and so on until the eighth bit which corresponds to blackout subframe SB following subframe SH5. Each of the other gray scale levels is obtained using a similar series of eight bits with the pixel being turned ON during the appropriate subframes.

In Figure 12, the example given for a single pixel operating using the hybrid scheme to obtain gray scale is expanded to illustrate the operation of the three pixel by three pixel array shown in Figure 4. The graph in Figure 12 illustrates the ON/OFF state of each of the three rows of pixels relative to time with the rows of pixels being arranged in order, row by row from top to bottom with pixel row R1 being the top pixel row and pixel row R3 being the bottom pixel row shown in Figure 4. Each of the individual pixels within each pixel row simultaneously receives the same data and therefore has a graph identical to the graph described above in Figure 11 with the overall frame being divided into subframes with their corresponding blackout subframes as described above. However, the beginning of each of the pixel row graphs corresponding to pixel rows R1 through R3 are shifted timewise a predetermined amount successively from pixel row R1 to pixel row R2 and pixel row R2 to pixel row R3. This is because, as described above, the data writing arrangement writes each pixel row without the use of any buffer circuitry within the individual pixel subcircuits, and therefore takes a certain amount of time to write each pixel row. As described in detail above for the time domain scheme and the brightness domain schemes, for the overall display to function properly, all of the pixel rows in the array must be written within the time of any single subframe.

Although only one specific example of the hybrid scheme has been given, it should be understood that it may take on a wide variety of forms and still remain within the scope of the present invention. Any combination of the time domain scheme and either of the described brightness domain schemes may be envisioned. Also,

although the example given provided thirty two levels of gray scale, it should be understood that the present invention is not limited to this number but instead, one of the reasons for using the hybrid scheme is to allow for a greater number of gray scale levels without requiring a subframe which is shorter than the time required for the FLC material to switch its ON/OFF state. Also, although the time domain subframes were described as doubling in length from subframe to subframe and the brightness domain subframes were described as doubling in brightness from subframe to subframe, it should be understood that this doubling is not a requirement of the present invention. Instead, the time differences from subframe to subframe for the time domain scheme and the brightness difference and the time length for each subframe from subframe to subframe for the brightness domain may vary widely and still remain within the scope of the present invention.

Now that the data ordering schemes for obtaining gray scale have been described in detail, the data ordering schemes for obtaining a color version of a display designed in accordance with the present invention will be described. Again as will be described in more detail hereinafter, the speed of the FLC/VLSI spatial light modulator also permits color images to be displayed using data ordering techniques. In accordance with the present invention, this is done by first dividing the frames into subframes for purposes of obtaining color and by changing the color of the light directed into the FLC/VLSI spatial light modulator for each of the color subframes. These color subframes are further divided into sub-subframes for purposes of obtaining gray scale color. This arrangement for obtaining a color display has a potentially significant advantage over prior art display systems since the same FLC/VLSI spatial light modulator may be used for either a monochrome display or a color display. Only the illuminator arrangement and data ordering technique must be changed to create a color display. This arrangement also allows the entire pixel to be used for each of the various colors, and does not require the pixel to be subdivided into separate color specific subpixels as is the case for some types of displays, thereby improving the resolution. By not having to subdivide the pixels, the pixels may be made smaller, further improving resolution and the potential compactness of the display.

As will be described in more detail hereinafter, for a color display, each frame is first divided into subframes for purposes of obtaining color. During each of these subframes, the illuminator arrangement directs light of a preselected color into the

spatial light modulator. For example, the frame may be divided into three subframes in which red light, green light, and blue light is successively directed into the modulator during its respective subframe. Each subframe is then divided into sub-subframes using the time domain, brightness domain, or hybrid data ordering schemes generally
5 described above for purposes of obtaining gray scale for each color. Because of the frame rate, and because each of the colors is presented in the appropriate gray scale for each frame, the viewer's eye perceives the integrated colors and brightness of each frame, thereby perceiving the desired gray scale color image.

Referring to Figures 13 and 14, the operation of a color version of a
10 display using the hybrid scheme will be described. In this embodiment of the present invention, frame FC is divided into three subframes C1, C2, and C3 for purposes of obtaining color. As was described above for switching the brightness of light, a blackout sub-subframe SB is used at the end of each color subframe in order to provide time to change the color of the light directed into the modulator without interfering with
15 the proper gray scale levels for each color. In the case of the hybrid scheme, the blackout sub-subframe also allows the brightness to change if the first described brightness domain approach is used. As illustrated in Figure 13, each color subframe is further divided into sub-subframes as described above for the frames of the hybrid scheme for purposes of obtaining color gray scale. Also as described above for the
20 hybrid scheme, a blackout sub-subframe SB is used after each of the sub-subframes between which the brightness of the light is to be changed, providing time for the brightness of the colored light to be changed. Alternatively, as was described for the second brightness domain scheme, the blackout sub-subframes using the pixels themselves may be eliminated if the light is turned OFF during the time in which the
25 array of pixels are being written and fully responding along with during the time the color of light directed into the array is changed.

As described above for each frame FH in Figure 11, each color subframe C1 through C3 of Figure 13 is divided into sub-subframes SS1 through SS5 which correspond to SH1 through SH5 in Figure 11. Also, for this particular embodiment, as
30 described for frame FH in Figure 11, each color subframe of Figure 13 includes three blackout sub-subframes SB positioned after sub-subframes SS1, SS2, and SS5 which are the sub-subframes after which the light directed into the pixel needs to be changed in either brightness or color. Each of these sub-subframes is of the length and has a

brightness associated with it as was described above in detail for their associated subframes in Figure 11 illustrating the hybrid scheme for obtaining gray scale.

With the above described arrangement of color subframes and gray scale sub-subframes, data writing arrangement 14 is able to write the pixel using a binary bit (i.e. a zero or a one) for each of the five sub-subframes and three blackout sub-subframes making up each of the three color subframes and therefore the overall frame FC. By controlling the ON/OFF state of the pixel during each of the sub-subframes and blackout sub-subframes of color subframes C1 through C3, thirty two gray scale levels of each of the three colors having equal changes in gray scale from level to level are provided. For example, as shown in Figure 13, gray scale level 23 is obtained for each of the colors by having data writing arrangement 14 provide three series of eight binary bits 1-0-1-0-1-0-1-0 (one eight bit series for each color subframe) with the first bit of each series corresponding to its respective sub-subframe SS1, the second bit corresponding to blackout sub-subframe SB following SS1, and so on until the eighth bit of each series which corresponds to blackout sub-subframe SB following sub-subframe SS5 of each color subframe C1 through C3. Each of the other gray scale levels for each color is obtained using a similar series of eight bits with the pixel being turned ON during the appropriate sub-subframes.

In Figure 14, the example given for a single color pixel operating using the hybrid scheme to obtain gray scale is expanded to illustrate the operation of the three pixel by three pixel array shown in Figure 4. The graph in Figure 14 illustrates the ON/OFF state of each of the rows of three pixels relative to time with the pixel rows being arranged in order, row by row from top to bottom with pixel row R1 being the top pixel row and pixel row R3 being the bottom pixel row shown in Figure 4. Each of the individual pixels within each pixel row simultaneously receives the same data and therefore has a graph identical to the graph described above in Figure 13 with the overall frame being divided into subframes and each of the subframes being divided into sub-subframes with their corresponding blackout sub-subframes as described above. However, the beginning of each of the pixel rows graphs corresponding to pixel row R1 through R3 are shifted timewise a predetermined amount successively from pixel row R1 to pixel row R2 and pixel row R2 to pixel row R3. This is because, as described above, the data writing arrangement writes each pixel row without the use of any buffer circuitry within the individual pixel subcircuitry, and therefore takes a certain amount

of time to write each pixel row. As described in detail above for the time domain scheme and the brightness domain scheme, for the overall display to function properly, all of the pixel rows in the array must be written within the time of any single sub-subframe.

5 In the above described example of a color version of the present invention using the hybrid scheme for obtaining gray scale for each color, thirty two levels of gray scale were provided. However, because of the speed at which the FLC material can switch, even broader gray scale levels may be achieved. For example, a frame rate of 60 frames per second provides for a frame lasting 16.7 milliseconds. If three colors are
10 used, each color subframe would be approximately 5.5 milliseconds. Each color subframe may be divided according to the hybrid scheme of the present invention to provide, for example, 128 levels of gray scale (requiring 7 subframes) for each color using the brightness domain scheme for the two shortest or least significant sub-subframes and the time domain scheme for the remaining five sub-subframes. Using the
15 FLC response time of 100 microseconds as the time for the shortest sub-subframes (this would include the two brightness domain sub-subframes and the shortest time domain sub-subframe), the longest time domain sub-subframe would be 1600 microseconds long (sixteen times as long as the shortest subframe). Therefore, this hybrid scheme would require approximately 3.9 milliseconds for each color subframe which is made up of two
20 100 microsecond brightness domain sub-subframes, two 200 microsecond blackout sub-subframes after each brightness domain sub-subframe for changing the brightness of the light, the five time domain sub-subframes (100, 200, 400, 800, and 1600 microseconds respectively), and a 200 microsecond blackout sub-subframe for changing the color of the light at the end of the color subframe. This required 3.9 milliseconds for this
25 example is well within the 5.5 milliseconds available.

Referring now to Figure 15, an alternative approach to producing a color system will be described. As shown in Figure 15 and as described above for Figure 13 and 14, each frame is divided into three color subframes C1, C2, and C3. However, in this approach, only the time domain scheme is used to further subdivide each color
30 subframe into sub-subframes for purposes of obtaining color gray scale. As described above, in the preferred embodiment, each of the successive sub-subframes is twice as long as its previous sub-subframe, therefore, providing the linear gray scale as was described above in detail for the time domain scheme. As mentioned above for the other

color versions, a blackout sub-subframe is provided at the end of each color subframe for changing the color of the light.

Although only two specific examples of a color version of the present invention have been given, it should be understood that it may take on a wide variety of forms and still remain within the scope of the present invention. Any combination
5 of the time domain scheme and either of the described brightness domain schemes may be envisioned. In fact, the present invention contemplates a color version using only the time domain scheme (as described above) or only the brightness domain scheme for controlling the ON/OFF states of the pixels. Also, although the hybrid scheme example
10 given provided thirty two levels of gray scale and the time domain scheme example provided eight levels of gray scale, it should be understood that the present invention is not limited to these numbers. In fact, the number of sub-subframes may be increased to provide any number of levels of gray scale and still remain within the scope of the present invention. However, one of the reasons for using the hybrid scheme, as was
15 done in one of the examples given, is to allow for a greater number of gray scale levels. As mentioned above, this approach increases the number of sub-subframes which will fit within a given frame, compared to the time domain scheme alone, without requiring a subframe which is shorter than the time required for the FLC material to switch its ON/OFF state.

Referring now to Figures 16A-C, as mentioned above, another advantage
20 of the hybrid system is that it allows the near full utilization of the available light directed into the spatial light modulator. Figures 16A-C illustrate the illuminator output for an FLC spatial light modulator pixel controlled by a five subframe arrangement using the time domain scheme, brightness domain scheme, and hybrid scheme, respectively.
25 In Figure 16A, for the time domain scheme, the illuminator is on at its maximum brightness throughout the operation of the pixel. Thus, the pixel's maximum brightness, as represented by the crosshatched area, is as high as possible. Although this provides excellent illuminator utilization, as described above, because each additional subframe added to obtain more levels of gray scale requires a subframe half as long as the next
30 shortest subframe, this scheme can only be used up to a limited number of gray scale levels as mentioned above. In Figure 16B, for the brightness domain scheme, the illuminator's brightness is cut by one half for each consecutive subframe. This underutilizes the potential of the illuminator and results in the pixel's maximum brightness

being less than the illuminator is capable of providing. However, in Figure 16C, for the hybrid scheme, the brightness domain approach is used for the first two subframes and the time domain approach is used for the remaining three subframes. This better utilizes the illuminator potential without having the problem of being restricted by the length of the shortest subframe.

While the embodiments illustrated throughout this disclosure have shown the subframes or sub-subframes being in a particular order, this is not a requirement of the present invention. For instance, in the case of the time domain scheme, the subframes were shown in order from shortest subframe to longest subframe. Also, in the case of the color system using the hybrid scheme, the color subframes and gray scale sub-subframes were shown in a particular order having all of the sub-subframes associated with one color grouped together in order from shortest to longest. However, the present invention would equally apply regardless of the order of the subframes and sub-subframes. For example, in the color system, the gray scale sub-subframes of different color subframes may be intermingled in any way so long as each of the sub-subframes is present within each overall frame.

Also, as described above, the present invention contemplates the use of FLC material as the preferred light modulating medium of the present invention. Present day technology in the field of FLC materials requires that in order to maintain a long life expectancy for the FLC material, the ON/OFF state of the material should be controlled such that the ON/OFF state of the material is balanced between the ON state and the OFF state. In other words, the state of the material should be ON in an equal amount of time as the state of the material is OFF. Applicants have found that certain novel approaches for balancing the ON/OFF state of the FLC material may be incorporated into the present invention to extend the life of the FLC material. These novel approaches are disclosed in copending United States Patent Application Number 08/361,775, Attorney Docket Number DIS1P006 entitled DC FIELD-BALANCING TECHNIQUE FOR AN ACTIVE MATRIX LIQUID CRYSTAL IMAGE GENERATOR filed coterminously herewith, which application is incorporated herein by reference.

Although only a few specific embodiments of the present invention have been described in detail, it should be understood that the present invention may be embodied in many other specific forms without departing from the spirit or scope of the invention. For instance, although the present invention has been described as a

miniature display, it is not limited to such a device, but instead is also particularly well suited to small portable hand held displays and larger very high resolution displays in which it is desirable to have a very high density of pixels. Alternatively, the present invention may be used for driving a printer, so long as the light-sensitive medium
5 being printed on has the ability to integrate the modulated image of the present invention. In the case of a human viewing a display, the viewer's eye integrates the modulated image. In the case of the printer, the light sensitive medium itself will by means of integration respond to the gray scale levels provided by the device designed in accordance with the present invention. Also, it should be understood that the present
10 invention may be embodied in the form of a projection display or may drive other devices which use optical images as their input.

Furthermore, as mentioned above, although the data writing arrangement has been described throughout this description as writing the pixels row by row, this is not a requirement of the present invention. Instead, the pixels may be written in any
15 desired sequence whether that is individual pixel by pixel, column by column, or some other sequence of specific groups of pixels. Also, although only one specific version of the hybrid scheme and the color version of the display using the hybrid scheme have been described, it should be understood the hybrid scheme and the color version of the display may incorporate a wide variety of combinations of the basic time domain scheme
20 and either of the brightness domain approaches depending on the requirements of the specific embodiment without departing from the spirit or scope of the present invention.

Therefore, the present examples are to be considered as illustrative and not restrictive, and the invention is not to be limited to the details given herein, but may be modified within the scope of the appended claims.

CLAIMS

1. A system for producing modulated light having gray scale, said system comprising:
- (a) an active matrix liquid crystal spatial light modulator having
 - (i) light modulating means including a layer of ferroelectric liquid crystal material which is designed to switch between ON and OFF states, and
 - (ii) active matrix means including VLSI circuitry for dividing said layer of liquid crystal material into an array of individual liquid crystal pixels and for causing each of said pixels of liquid crystal material to modulate light individually by switching between said ON and OFF states in a way that depends upon the data which the VLSI circuitry is written;
 - (b) illumination means including a light source for directing light from said source into said pixel-divided layer of ferroelectric liquid crystal material in a specific way; and
 - (c) means for writing said VLSI circuitry with preselected data in accordance with a particular data ordering scheme such that said circuitry, in response to the data, causes said pixels of liquid crystal material to individually switch between their ON and OFF states and therefore modulate light from said source in a way which, depending upon said data, produces a specific overall pattern of gray scale light.
2. A system according to Claim 1 wherein said active matrix means divides said layer of liquid crystal material into an array of immediately adjacent pixels, each of which has a maximum light modulating area of at most 100 square microns.
3. A system according to Claim 2 wherein each of said pixels is square shaped, at most 10 microns on a side.
4. A system according to Claim 1 wherein said VLSI circuitry of said active matrix means includes an array of subcircuits, each of which is dedicated to and defines a specific one of said pixels, each of said subcircuits including one and only one active component for modulating its associated pixel.

5. A system according to Claim 4 wherein said active component of each subcircuit is a transistor.

6. A system according to Claim 1 wherein said layer of ferroelectric liquid crystal material is maintained at a substantially uniform thickness of at most approximately 2 microns, whereby the size of said pixels can be made smaller than would be the case if the layer of liquid crystal were much thicker, for example of the order of 6 microns as required by nematic liquid crystal material in spatial light modulators.

7. A system according to Claim 1 wherein said VLSI circuitry of said active matrix means includes an array of subcircuits, each subcircuit of which is dedicated to and defines a specific one of said pixels, and wherein said particular data ordering scheme is such that said data writing means writes each of said subcircuits and therefore each of said pixels with preselected data in a predetermined successive order, timewise, such that none of said subcircuits require auxiliary data storage buffer circuitry and therefore the complexity of said array of subcircuits can be reduced.

8. A system according to Claim 7 wherein said preselected data is digital data divided into individual digital data commands, one for each of said subcircuits and therefore each of said pixels, during any given time frame, each digital data command establishing the length of time its associated pixel is in its ON and OFF state during a given time frame.

9. A system according to Claim 8 wherein said light source is a constant light source, wherein each of said given time frames is divided into a plurality of subframes for purposes of obtaining gray scale, and wherein each of said digital data commands establishes whether its associated pixel is in its ON or OFF state during each subframe of any given frame, whereby whether any given pixel is in its ON or OFF state during the various subframes of a given time frame determines the overall gray scale of that pixel during that time frame.

10. A system according to Claim 9 wherein said plurality of subframes

are a plurality of subframes of unequal lengths of time.

11. A system according to Claim 9 wherein each of said digital commands comprises a series of binary bits, one bit for each of said subframes, each binary bit establishing whether its associated pixel is in its ON or OFF state during its
5 associated subframe.

12. A system according to Claim 10 wherein the duration of the subframes, if arranged in order of duration, increase by a factor of two.

13. A system according to Claim 8 wherein said illumination means includes means for varying the intensity of light directed from said light source into said
10 pixel-divided layer of liquid crystal material in a predetermined way, wherein each of said given time frames is divided into a plurality of subframes for purposes of obtaining gray scale, and wherein each of said digital data commands establishes whether its associated pixel is in its ON or OFF state during each subframe of any given frame.

14. A system according to Claim 13 wherein said illumination means
15 includes means for directing light of different intensities from said light source into said pixel-divided layer of liquid crystal material during each subframe of any given frame, whereby whether any given pixel is in its ON or OFF state during the various subframes of a given time frame determines the overall gray scale of that pixel during that time frame.

20 15. A system according to Claim 14 wherein said plurality of subframes is a plurality of subframes of equal time periods.

16. A system according to Claim 14 wherein each of said digital data commands comprises a series of binary bits, one bit for each of said subframes, each binary bit establishing whether its associated pixel is in its ON or OFF state during its
25 associated subframe.

17. A system according to Claim 15 wherein said different intensities

double from subframe to subframe when said subframes are arranged in a particular order.

18. A system according to Claim 16 wherein each of said frames includes an additional blackout subframe between each of said first-mentioned subframes and wherein each of said digital data commands includes a binary bit associated with each of said blackout subframes for maintaining its associated pixel in an OFF state during its associated blackout subframe, each of said blackout subframes being sufficiently long to allow all of the pixels written during each immediately previous first-mentioned subframe to fully respond and to allow said illumination means to switch the intensity of said light source before the writing means begins writing the pixels in the next first-mentioned subframe.

19. A system according to Claim 8 wherein each of said given time frames is divided into a plurality of subframes for purposes of obtaining color, wherein each of said subframes is divided into a plurality of sub-subframes of predetermined time periods for purposes of obtaining colored gray scale, wherein said illumination means includes means for alternatively directing light of different colors, equal in number to the number of said subframes, into said pixel-divided layer of liquid crystal material during each successive subframe of each frame, and wherein each of said digital data commands establishes whether its associated pixel is in its ON or OFF state during each subframe and sub-subframe of any given frame, whereby whether any given pixel is in its ON or OFF state during the various subframes and sub-subframes of a given time frame determines the overall gray scale and color of that pixel during that time frame.

20. A system according to Claim 19 wherein each of said digital commands comprises a series of binary bits, one for each of said sub-subframes, each binary bit establishing whether its associated pixel is in its ON or OFF state during its associated sub-subframe.

21. A system according to Claim 20 wherein each of said subframes includes an additional blackout subframe after each of said first-mentioned subframes and wherein each of said digital data commands includes a binary bit associated with each

of said blackout subframes for maintaining its associated pixel in an OFF state during its associated blackout subframe, each of said blackout subframes being sufficiently long to allow all of the pixels written during each immediately previous subframe to respond fully and to allow said illumination means to switch colors before the writing means
5 begins writing the pixels in the next subframe .

22. A system according to Claim 1 wherein said illumination means including said light source includes means for directing light of different colors into said pixel-divided layer of ferroelectric liquid crystal material in a specific way so as to produce modulated colored light having gray scale and a polarizer/analyzer designed to
10 pass light across a broad color band including light of said different colors.

23. A system according to Claim 1 wherein said illumination means includes at least one light emitting diode serving as said light source.

24. A system according to Claim 23 wherein said light emitting diode is the sole source of light of said illumination means.

15 25. A system according to Claim 23 wherein said illumination means includes at least three light emitting diodes serving as said light source, each of which is a different color from the others.

26. A system according to Claim 25 wherein said three light emitting diodes are red, green and blue light emitting diodes.

20 27. A system according to Claim 1 including means for viewing the light modulated by said layer of liquid crystal material, whereby said system serves as a light display.

28. A system for producing modulated light having gray scale, said system comprising:

- 25 (a) an active matrix liquid crystal spatial light modulator having
(i) light modulating means including a layer of ferroelectric

liquid crystal material which is designed to switch between ON and OFF states, said layer of liquid crystal material having a substantially uniform thickness of at most approximately 2 microns; and

(ii) active matrix means including VLSI circuitry for dividing
5 said layer of liquid crystal material into a rectangular array of immediately adjacent liquid crystal pixels, each of which has a maximum light modulating area of at most approximately 100 square microns, and for causing each of said pixels of liquid crystal material to modulate light individually by switching between said ON and OFF states in a way that depends upon the manner in which the VLSI circuitry is written, said VLSI
10 circuitry including an array of subcircuits, each of which is dedicated to and defines a specific one of said pixels, each of said subcircuits including one and only one active component for modulating its associated pixel, whereby to reduce the space required by each subcircuit and thereby reduce the size of each of said pixels;

(b) illumination means including a light source for directing light from
15 said source into said pixel-divided layer of ferroelectric liquid crystal material in a specific way; and

(c) means for writing said VLSI circuitry with preselected data in accordance with a particular data ordering scheme such that said circuitry, in response to the data, causes said pixels of liquid crystal material to individually switch between
20 their ON and OFF states and therefore modulate light from said source in a way which, depending upon said data, produces a specific overall pattern of gray scale light.

29. A system according to Claim 28 wherein said illumination means including said light source includes means for directing light of different colors into said pixel-divided layer of ferroelectric liquid crystal material in a specific way so as to
25 produce modulated colored light having gray scale and a polarizer/analyzer designed to pass light across a broad color band including light of said different colors.

30. A system according to Claim 28 wherein said illumination means includes at least one light emitting diode serving as said light source.

31. A system according to Claim 30 wherein said light emitting diode is the sole source of light of said illumination means.

32. A system according to Claim 28 wherein said illumination means includes at least three light emitting diodes serving as said light source, each of which
5 is a different color from the others.

33. A system according to Claim 32 wherein said three light emitting diodes are red, green and blue light emitting diodes.

34. A system according to Claim 28 including means for viewing the
10 light modulated by said layer of liquid crystal material, whereby said system serves as a light display.

35. A system for producing modulated light having gray scale, said system comprising:

(a) an active matrix liquid crystal spatial light modulator having
15 (i) light modulating means including a layer of ferroelectric liquid crystal material which is designed to switch between ON and OFF states, said layer of liquid crystal material having a substantially uniform thickness of at most approximately 2 microns; and

(ii) active matrix means including VLSI circuitry for dividing
20 said layer of liquid crystal material into a rectangular array of immediately adjacent liquid crystal pixels, each of which has a maximum light modulating area of at most approximately 100 square microns, and for causing each of said pixels of liquid crystal material to modulate light individually by switching between said ON and OFF states in a way that depends upon the manner in which the VLSI circuitry is written, said VLSI
25 circuitry including an array of subcircuits, each of which is dedicated to and defines a specific one of said pixels, each of said subcircuits including one and only one active component for modulating its associated pixel, whereby to reduce the space required by each subcircuit and thereby reduce the size of each of said pixels;

(b) illumination means including a light source consisting of light
30 emitting diode means for directing light from said source into said pixel-divided layer

of ferroelectric liquid crystal material in a specific way; and

(c) means for writing said VLSI circuitry with preselected data in accordance with a particular data ordering scheme such that said circuitry, in response to the data, causes said pixels of liquid crystal material to individually switch between their ON and OFF states and therefore modulate light from said source in a way which, depending upon said data, produces a specific overall pattern of gray scale light, said particular data ordering scheme being designed so that said data writing means writes each of said subcircuits and therefore each of said pixels with preselected data in a predetermined successive order, timewise, such that said VLSI circuitry does not require buffer subcircuitry for writing said subcircuits simultaneously and therefore the complexity of said array of subcircuits can be reduced, said preselected digital data being divided into individual digital data commands, one for each of said subcircuits and therefore each of said pixels, during any given time frame, each digital data command establishing the length of time its associated pixel is in its ON and OFF state during a given time frame.

36. A system according to Claim 35 wherein each of said given time frames is divided into a plurality of subframes for purposes of obtaining color, wherein each of said subframes is divided into a plurality of sub-subframes of predetermined time periods for purposes of obtaining colored gray scale, wherein said illumination means includes means for alternatively directing light of different colors, equal in number to the number of said subframes, into said pixel-divided layer of liquid crystal material during each successive subframe of each frame, and wherein each of said digital data commands establishes whether its associated pixel is in its ON or OFF state during each subframe and sub-subframe of any given frame, whereby whether any given pixel is in its ON or OFF state during the various subframes and sub-subframes of a given time frame determines the overall gray scale and color of that pixel during that time frame.

37. A system according to Claim 36 wherein each of said digital commands comprises a series of binary bits, one for each of said sub-subframes, each binary bit establishing whether its associated pixel is in its ON or OFF state during its associated sub-subframe.

38. A system according to Claim 37 wherein each of said subframes includes an additional blackout subframe after each of said first-mentioned subframes of equal time periods and wherein each of said digital data commands includes a binary bit associated with each of said blackout subframes for maintaining its associated pixel in an OFF state during its associated blackout subframe, each of said blackout subframes being sufficiently long to allow all of the pixels written during each immediately previous subframe of equal time period to respond fully and to allow said illumination means to switch colors before the writing means begins writing the pixels in the next subframe of equal time period.

39. An active matrix liquid crystal spatial light modulator for use in a system for producing modulated light having gray scale, said spatial light modulator comprising:

(a) light modulating means including a layer of ferroelectric liquid crystal material which is designed to switch between ON and OFF states, said layer of liquid crystal material having a substantially uniform thickness of at most approximately 2 microns; and

(b) active matrix means including VLSI circuitry for dividing said layer of liquid crystal material into a rectangular array of immediately adjacent liquid crystal pixels, each of which has a maximum light modulating area of at most approximately 100 square microns, and for causing each of said pixels of liquid crystal material to modulate light individually by switching between said ON and OFF states in a way that depends upon the manner in which the VLSI circuitry is written, said VLSI circuitry including an array of subcircuits, each of which is dedicated to and defines a specific one of said pixels, each of said subcircuits including one and only one active component for modulating its associated pixel, whereby to reduce the space required by each subcircuit and thereby reduce the size of each of said pixels.

40. A miniature display system comprising:

- a) a light source;
- b) a ferroelectric liquid crystal spatial light modulator including (i) a very-large-scale integration integrated circuit, active-matrix backplane having an array of individually addressable reflective pixels partially formed by metal pads on the upper

- surface of said integrated circuit, said metal pads having a cross sectional area less than 100 square microns and said metal pads acting both as mirrors and as ferroelectric liquid crystal electrodes, (ii) a thin layer of ferroelectric liquid crystal positioned directly atop said backplane, and (iii) a transparent substrate positioned atop said layer of ferroelectric liquid crystal; and
- 5 c) polarizing means for polarizing and selectively transmitting light from said light source into said spatial light modulator
- d) collimating means for collecting and directing light from said light source into said polarizing means;
- 10 e) an eyepiece lens for viewing said display.

41. A reflective spatial light modulator comprising:
- a) a very-large-scale integration integrated circuit, active-matrix backplane having an array of individually addressable reflective pixels partially formed by metal pads on the upper surface of said integrated circuit, said metal pads having a cross sectional area less than 100 square microns and said metal pads acting both as mirrors and as ferroelectric liquid crystal electrodes;
- 15 b) a thin layer of ferroelectric liquid crystal positioned directly atop said backplane, and
- c) a transparent substrate positioned atop said layer of ferroelectric liquid crystal.
- 20

42. A display system for producing an active matrix display having a certain frame rate, said system comprising:
- a) a spatial light modulator having an array of ferroelectric liquid crystal pixels each of which is adapted to be turned ON and OFF independently from the others during each of said frames;
- 25 b) illumination means including a source of light and means for coupling said light with said spatial light modulator in a way which causes said spatial light modulator to produce a visual display depending upon the ON and OFF state of the various pixels; and
- 30 c) means for selectively turning ON and OFF each of said pixels for a controlled portion of time during each of said frames.

43. A system as set forth in Claim 42 wherein said illumination means includes means for coupling substantially continuous light at a substantially constant brightness with said spatial light modulator whereby the gray scale of each of said pixels for each of said frames is proportional to said controlled portion of time during each
5 frame in which each pixel is turned ON.

44. A system as set forth in Claim 43 wherein said frames are divided into a plurality of subframes of different lengths of time such that a longest subframe is twice as long as a second longest subframe and each of the remaining subframes are half as long as their respective next longer subframe and wherein during said subframes, each
10 of said pixels has a selected ON/OFF state whereby said subframes provide binary control of said gray scale by selectively controlling the ON/OFF state of each pixel for each of said subframes.

45. A system as set forth in Claim 42 wherein said frames are divide into a plurality of subframes during which each of said pixels has a certain ON/OFF
15 state and wherein said illuminating means includes means for coupling a controllable brightness of light with said spatial light modular for each of said subframes whereby the gray scale of each of said pixels for each of said frames is proportional to the ON/OFF state of each of said pixels during each of said subframes in combination with the controllable brightness of light coupled with said spatial light modulator during each
20 of said subframes.

46. A system as set forth in Claim 45 wherein said subframes are of equal length of time and wherein said controllable brightness of light is varied such that it is brightest during a brightest subframe, it is half as bright during a second brightest subframe, and during each of the remaining subframes said controllable brightness of
25 light is half as bright as it is for the respective next brighter subframe whereby said subframes provide binary control of said gray scale by selectively controlling the ON/OFF status of each pixel for each of said subframes.

47. A system for producing modulated light, comprising:

(a) a spatial light modulator having an array of individual light

modulating pixels, each pixel of which includes

(i) a light modulating medium which is designed to switch between ON and OFF light modulating states for modulating light, depending upon its light modulating state, and

5 (ii) means for switching its associated light modulating medium between the medium's ON and OFF states in response to and dependent on externally generated, preselected data signals;

(b) means for generating a stream of said preselected data signals; and

(c) means for applying said preselected data signals to the switching
10 means associated with each of said pixels during each one of a series of successive time periods such that

(i) for any given successive time period, all of said pixels are first caused to modulate light, depending upon the states of their respective light modulating mediums and responsive to the data signals applied to the light modulating
15 mediums during the given period, and

(ii) thereafter during certain ones of said time periods, the entire array of pixels are switched to the OFF state, all at the same time, whereby to provide a pixel blackout subperiod during each of said certain time periods.

48. A system according to Claim 47 wherein all of said pixels are
20 turned OFF in response to said data signals to produce said blackout subperiod.

49. A system according to Claim 47 including a light source for providing light to said array of light modulating pixels wherein said light source is turned OFF to produce said blackout subperiod.

50. A system according to Claim 47 wherein said pixels are arranged
25 in specific groups of said pixels and wherein said signal applying means includes means for simultaneously applying said preselected data signals to the switching means associated with each of said groups, one group at a time, during each of said successive time periods.

51. A system according to Claim 50 wherein said pixels are arranged
30 in rows and columns and wherein said specific groups consist of the rows making up

said rows and columns.

52. A system according to Claim 47 wherein

(a) said series of successive time periods together form a number of successive groups of time periods which in turn make up a single time frame, each of
5 said time period groups corresponding to a particular one of a plurality of different colors and the last time period of each group of time periods serving as one of said certain time periods including a blackout subperiod; and

(b) said system further includes illumination means for alternatively directing light of each of said plurality of different colors to each of the pixel's light
10 modulating medium for modulation by the latter during successive time period groups of said time frame, said illumination mean including means for switching between said light colors from time period group to time period group during the blackout subperiods of each of said certain time periods.

53. A system according to Claim 52 wherein said pixels are arranged
15 in specific groups of said pixels and wherein said signal applying means includes means for simultaneously applying said preselected data signals to the switching means associated with each of said groups, one group at a time, during each of said successive time periods.

54. A system according to Claim 53 wherein said pixels are arranged
20 in rows and columns and wherein said specific groups consist of all of the rows making up said rows and columns.

55. A system according to Claim 52 wherein the time periods making up each of said time period groups are unequal in duration.

56. A system according to Claim 55 wherein the time periods making
25 up each of said time period groups, if arranged in order of duration, would double in duration from time period to time period.

57. A system according to Claim 47 wherein said light modulating

medium is ferroelectric liquid crystal material.

58. A system according to Claim 57 wherein said switching means includes active matrix VLSI circuitry.

59. A method of producing modulated light, comprising the steps of
5 (a) providing a spatial light modulator having an array of individual light modulating pixels, each pixel of which includes

(i) a light modulating medium which is designed to switch between ON and OFF light modulating states for modulating light directed into the pixel, and

10 (ii) means for switching its associated light modulating medium between the medium's ON and OFF states in response to and dependent on externally generated, preselected data signals;

(b) generating a continuous stream of said preselected data signals; and

(c) applying said preselected data signals to the switching means
15 associated with each of said pixels during each one of a series of successive time periods such that

(i) for any given successive time period all of said pixels are first caused to modulate light, depending upon the states of their respective light modulating mediums and responsive to the data signals applied to the light modulating
20 mediums during the given period, and

(ii) thereafter during certain ones of said time periods, said light modulating mediums of all of the pixels of the array of pixels are switched to their OFF state, all at the same time, whereby to provide a pixel blackout subperiod during each of said certain time periods.

25 60. A method according to Claim 59 wherein the step of applying said preselected data signals to the switching means includes the step of turning OFF all of said pixels in response to said data signals to produce said blackout subperiod.

61. A method according to Claim 59 including the step of providing a light source for directing light into said array of light modulating pixels wherein said light source is turned OFF to produce said blackout subperiod.

62. A method according to Claim 59 wherein said pixels are arranged
5 in specific groups of said pixels and wherein said step of applying said preselected data signals to the switching means associated with each of said pixels during each one of a series of successive time periods includes simultaneously applying said preselected data signals to the switching means associated with each of said groups, one group at a time, during each of said successive time periods.

10 63. A method according to Claim 62 wherein said pixels are arranged in rows and columns and wherein said specific groups consist of the rows making up said rows and columns.

64. A method according to Claim 59 wherein
(a) said series of successive time periods together form a number of
15 successive groups of time periods which in turn make up a single time frame, each of said time period groups corresponding to a particular one of a plurality of different colors and the last time period of each group of time periods serving as one of said certain time periods including a blackout subperiod; and

(b) said method further includes the steps of alternatively directing
20 light of each of said plurality of different colors to each of the pixel's light modulating medium for modulation by the latter during successive time period groups of said time frame, and switching between said light colors from time period group to time period group during the blackout subperiods of each of said certain time periods.

65. A method according to Claim 64 wherein the time periods making
25 up each of said time period groups are unequal in duration.

66. A method according to Claim 65 wherein the time periods making up each of said time period groups, if arranged in order of duration, would double in duration from time period to time period.

67. A field sequential system for producing color modulated light having color gray scale during a given period of time comprising;

(a) a spatial light modulator including light modulating means switchable between different states so as to act on light in different ways,

5 (b) means for switching said modulating means between different states in a controlled way during successive subperiods of said period, and

(c) illumination means including a semiconductor light source for directing light of a plurality of different colors alternately and successively into said modulating means during predetermined subperiods of said period.

10 68. A system according to Claim 67 wherein said plurality of different colors of light includes red light, green light, and blue light.

69. A system according to Claim 67 wherein said illumination means includes a plurality of LEDs, said plurality of LEDs generating said different colors of light.

15 70. A system according to Claim 69 wherein said plurality of LEDs include at least one red LED, at least one green LED, and at least one blue LED.

71. A field sequential system for producing modulated light having gray scale during a given period of time comprising;

20 (a) a spatial light modulator including light modulating means switchable between different states so as to act on light in different ways,

(b) means for switching said modulating means between different states in a controlled way during successive subperiods of said period, and

25 (c) illumination means including a semiconductor light source for directing light of constant intensity into said modulating means during predetermined subperiods of said period.

72. A system according to Claim 71 wherein said subperiods differ in time duration from one another.

73. A system according to Claim 72 wherein, if said subperiods differing in time duration are arranged successively timewise, the time duration of each successive subperiod making up said given period increases by a factor of two whereby to control gray scale.

5 74. A field sequential system for producing modulated light having gray scale during a given period of time comprising;

(a) a spatial light modulator including light modulating means switchable between different states so as to act on light in different ways,

(b) means for switching said modulating means between different states
10 in a controlled way during successive subperiods of said period, and

(c) illumination means for directing light of different intensities alternately and successively into said modulating means during predetermined subperiods of said period.

75. A system according to Claim 74 wherein said subperiods are equal
15 in time duration.

76. A system according to Claim 75 wherein, if said subframes are arranged successively in order with respect to the intensity of light said illumination means directs into said modulating means, said different intensities of light of each successive subperiod making up said given period increase by a factor of two whereby
20 to control gray scale.

77. A field sequential system for producing modulated light having gray scale during a given period of time comprising;

(a) a liquid crystal spatial light modulator including light modulating means switchable between different states so as to act on light in different ways,

(b) means for switching said modulating means between different states
25 in a controlled way during successive subperiods of said period, and

(c) illumination means for directing light of constant intensity into said modulating means during predetermined subperiods of said period.

78. A system according to Claim 77 wherein said subperiods differ in

time duration from one another.

79. A system according to Claim 78 wherein, if said subperiods differing in time duration are arranged successively timewise, the time duration of each successive subperiod making up said given period increases by a factor of two whereby
5 to control gray scale.

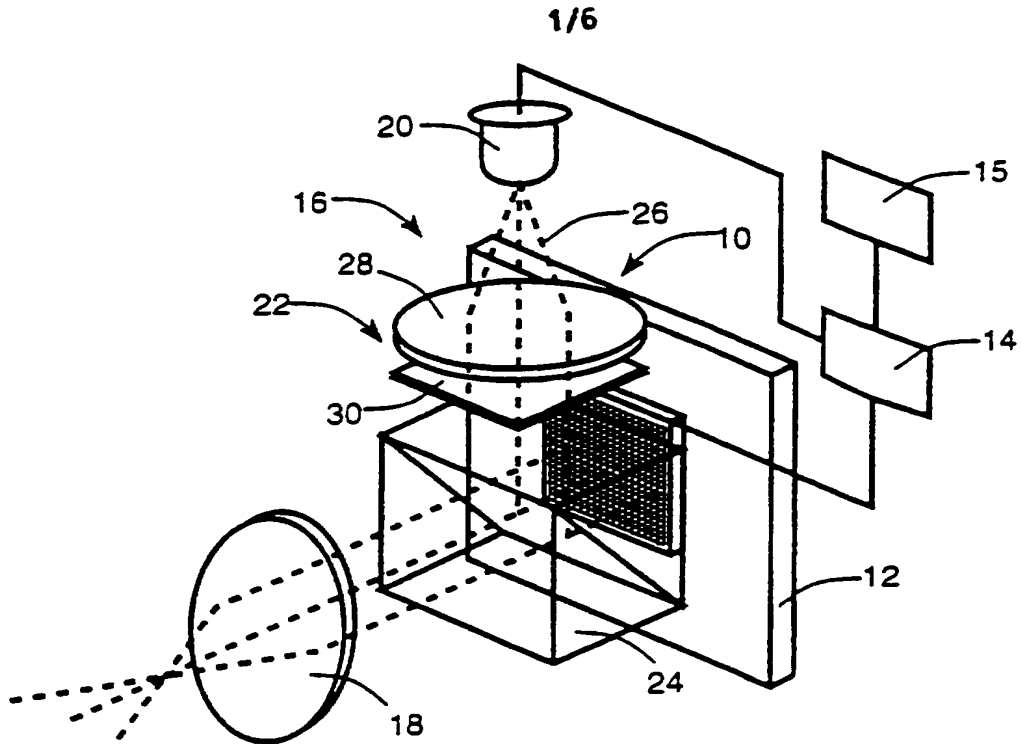


Figure 1

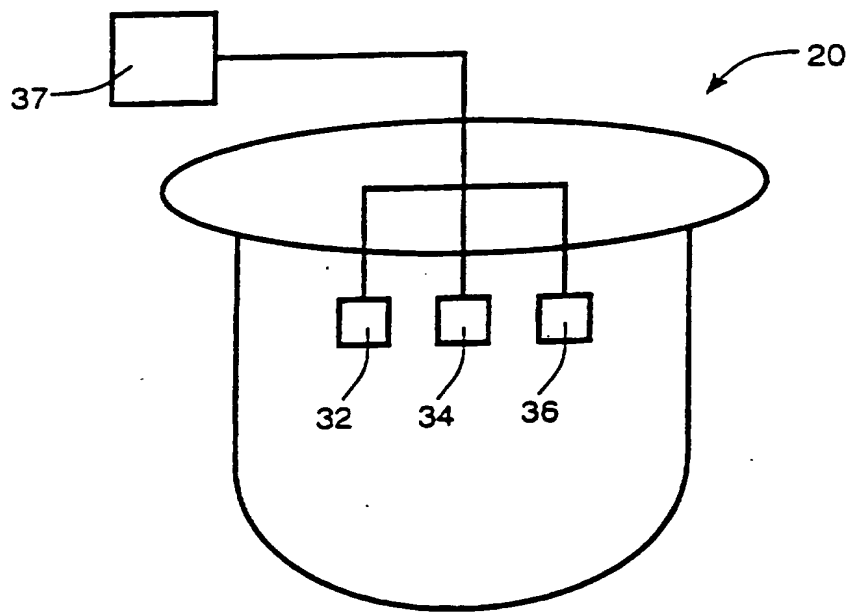


Figure 2

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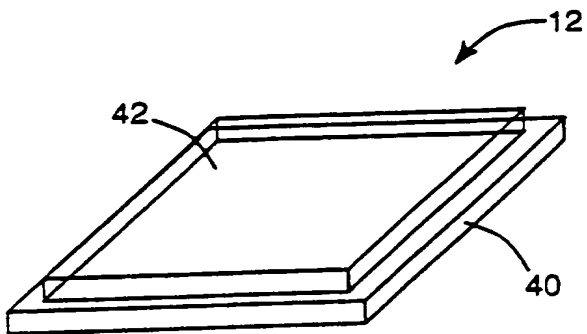


Figure 3A

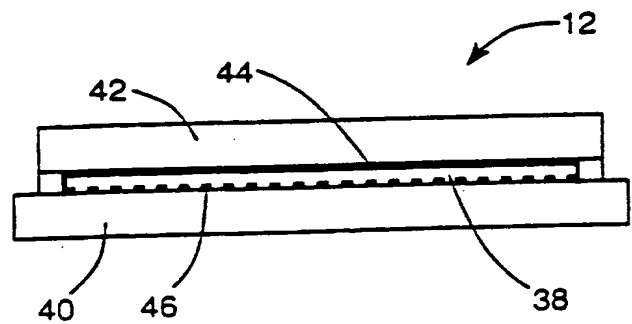


Figure 3B

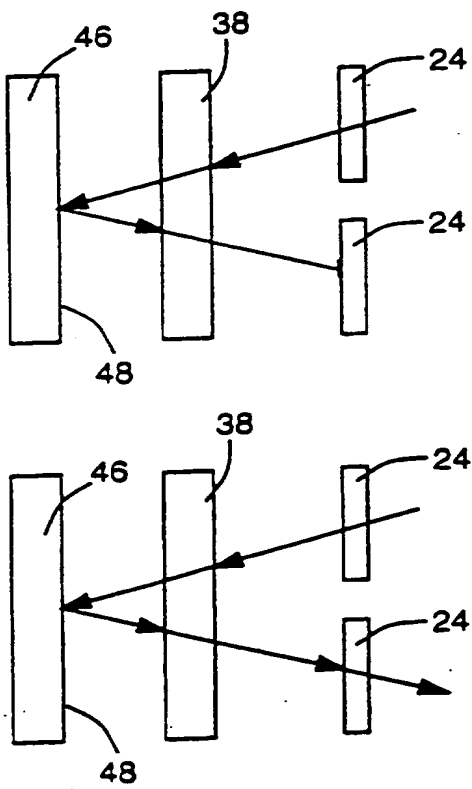


Figure 3C

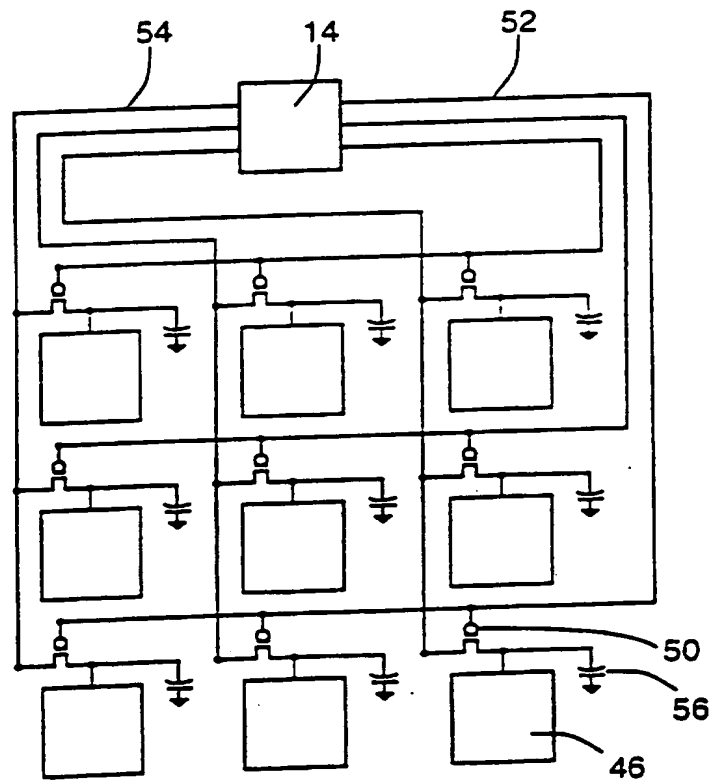


Figure 4

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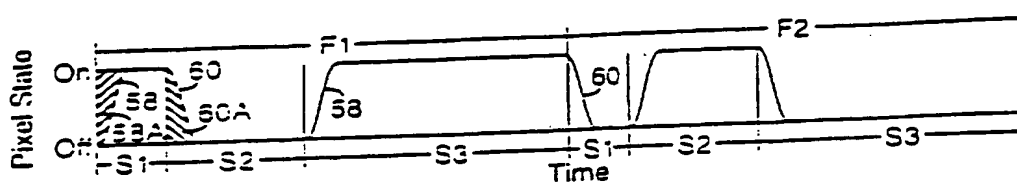


Figure 5

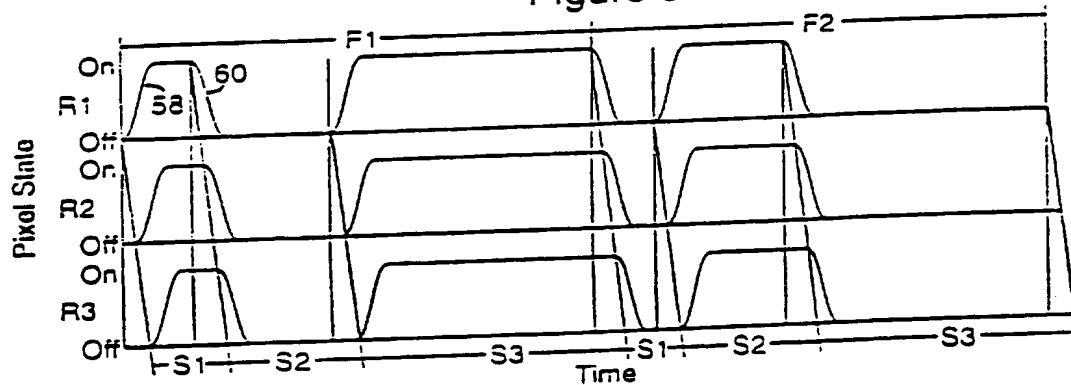


Figure 6

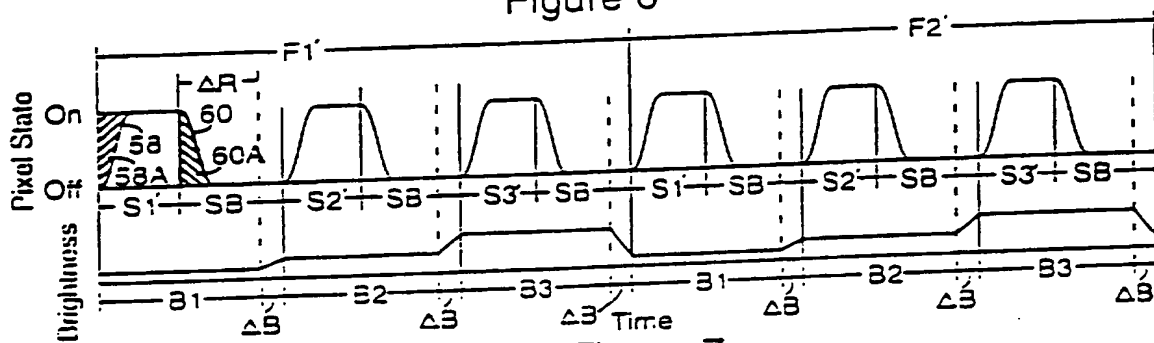


Figure 7

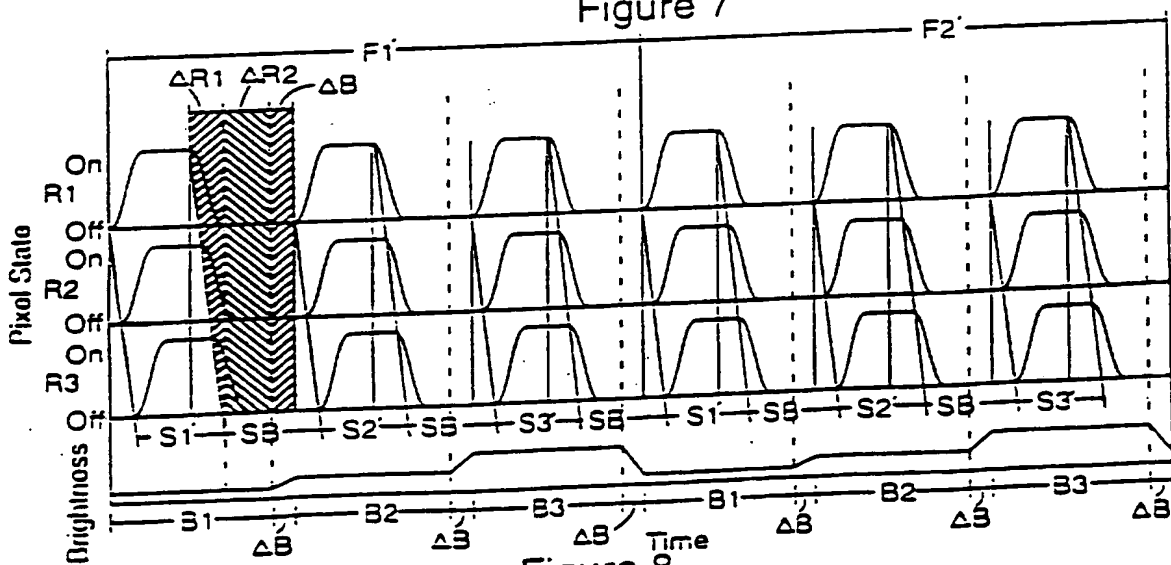


Figure 8

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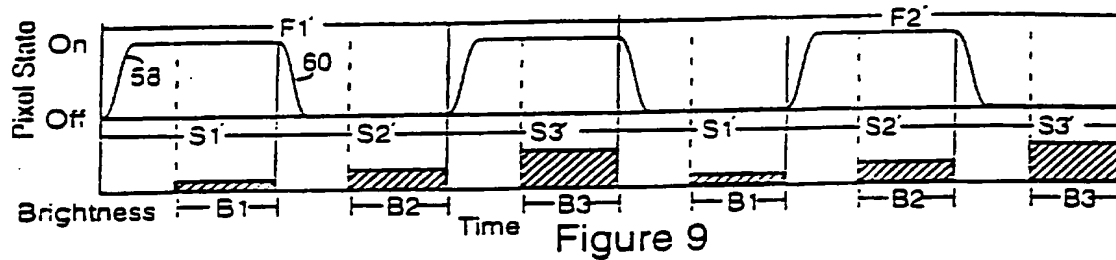


Figure 9

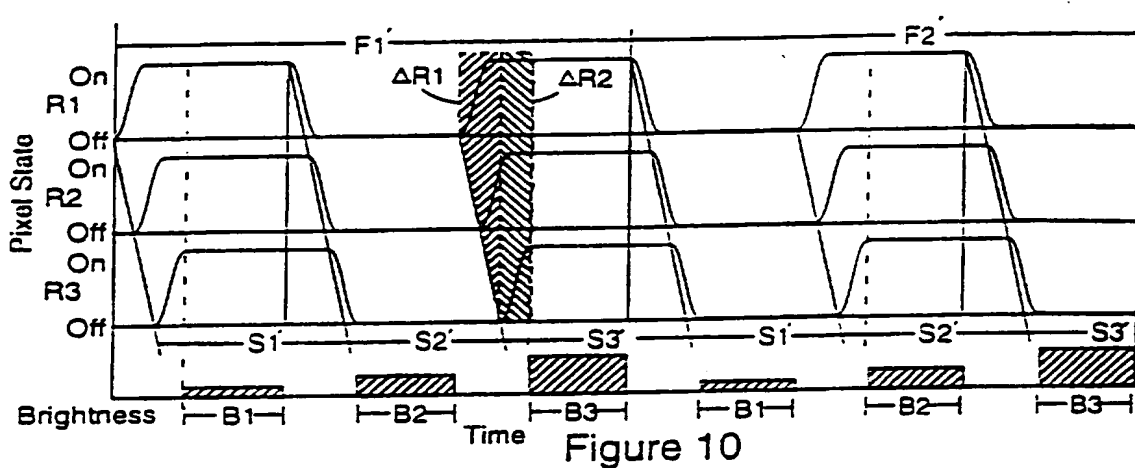


Figure 10

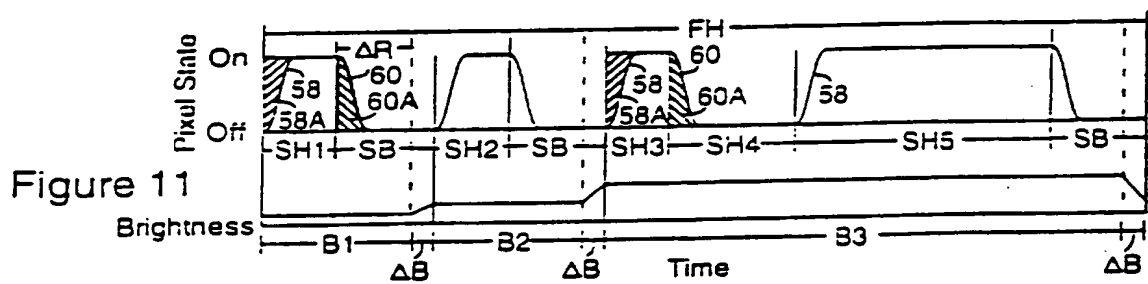


Figure 11

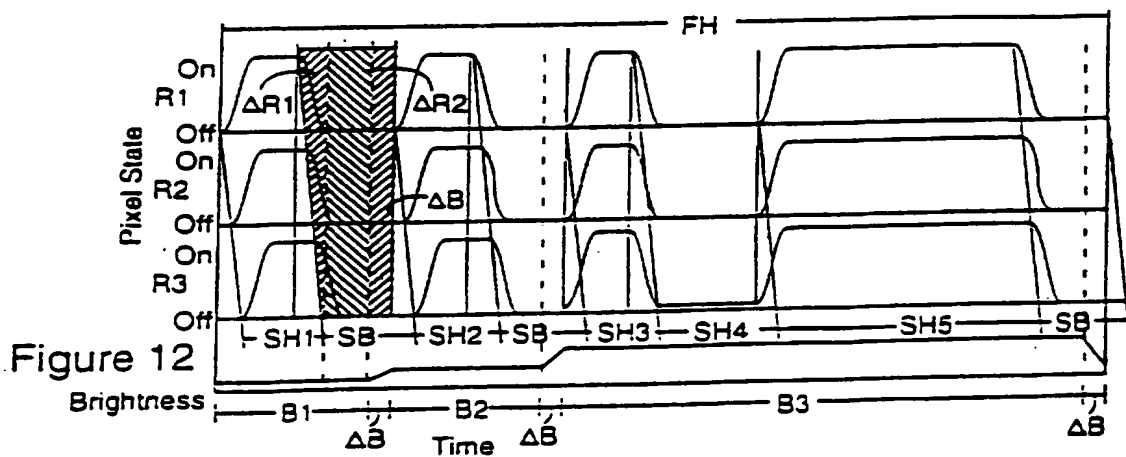


Figure 12

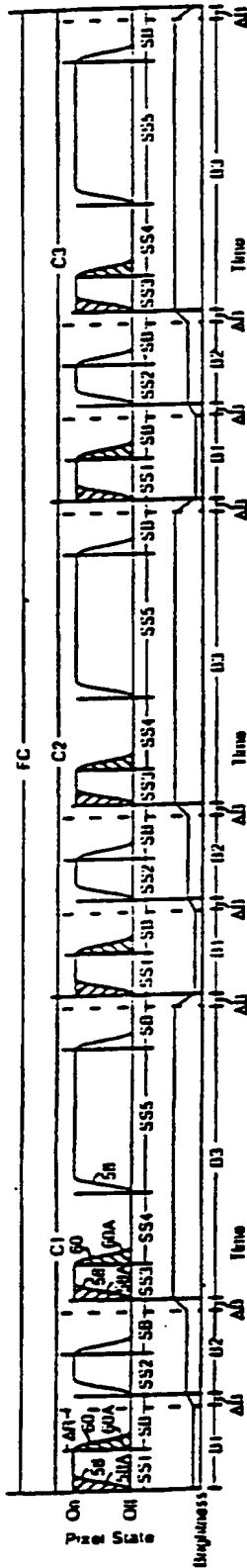


Figure 13

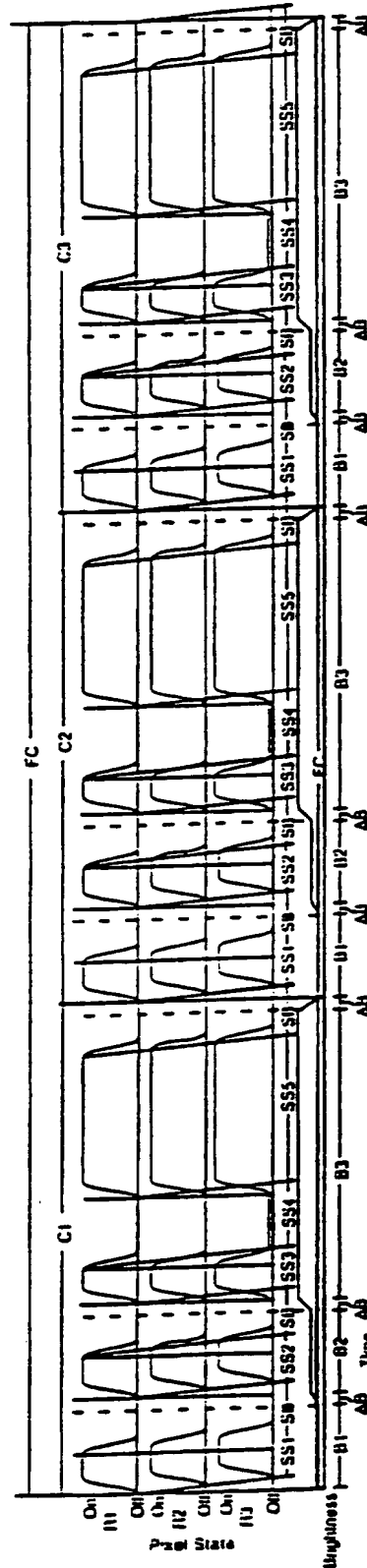


Figure 14

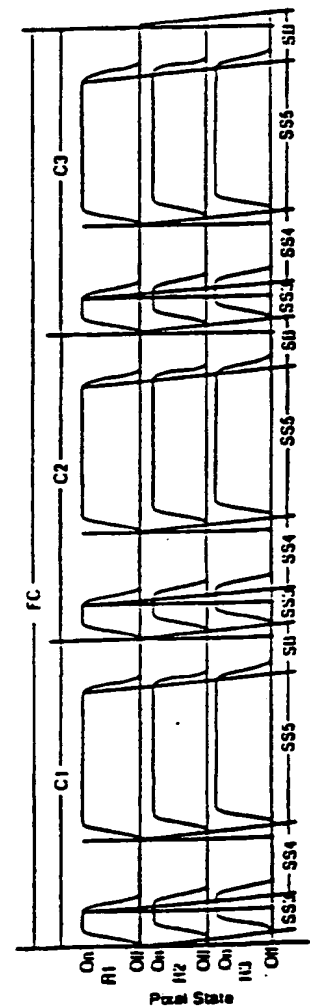


Figure 15

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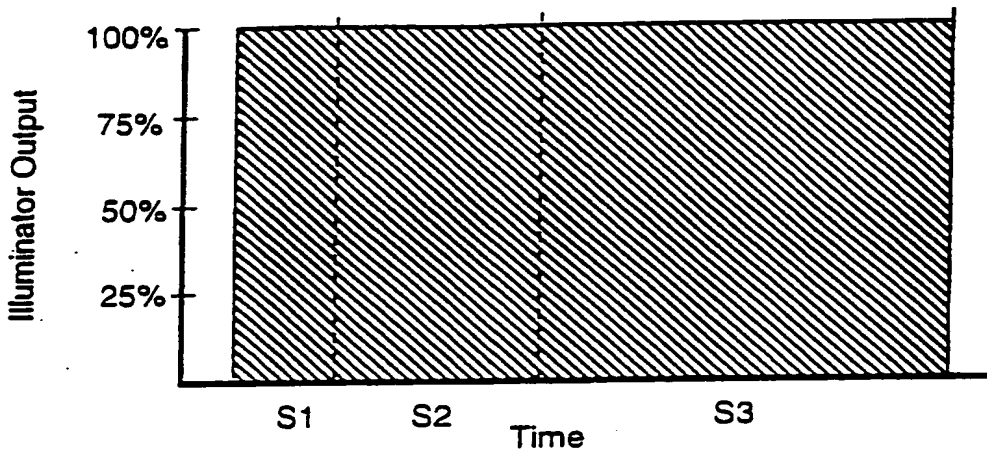


Figure 16A

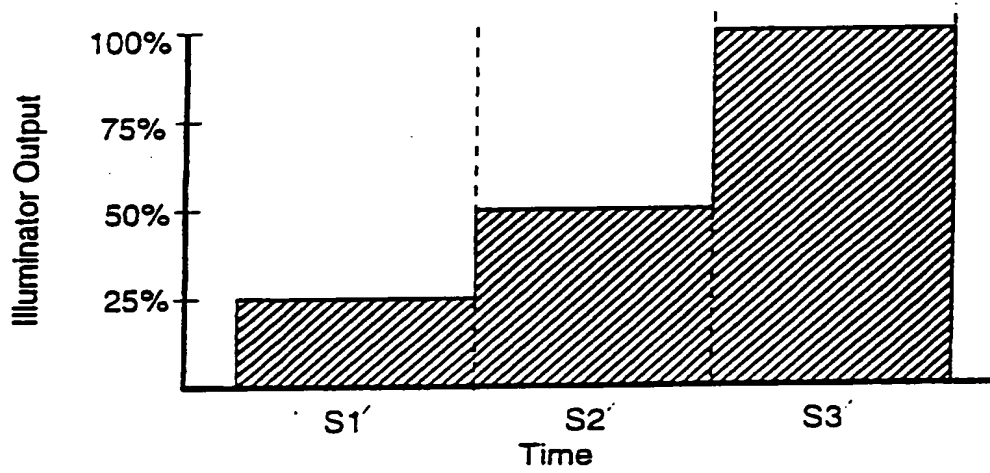


Figure 16B

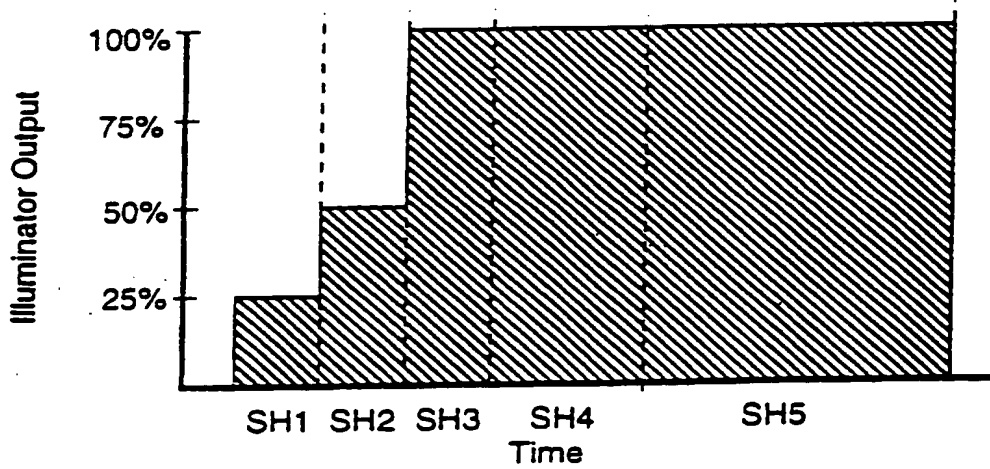


Figure 16C

INTERNATIONAL SEARCH REPORT

International Application No
PCT/US 95/16359

A. CLASSIFICATION OF SUBJECT MATTER
G 09 G 3/36

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

G 02 F, G 09 G

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practical, search terms used)

C. DOCUMENTS CONSIDERED TO BE RELEVANT

| Category * | Citation of document, with indication, where appropriate, of the relevant passages | Relevant to claim No. |
|------------|--|-----------------------|
| A | US, A, 5 128 782 (WOOD) 07 July 1992 (07.07.92), abstract. | 1 |
| | -- | |
| A | EP, A, 0 287 034 (SEIKO EPSON) 12 April 1988 (12.04.88), abstract. | 1 |
| | ---- | |

☐ Further documents are listed in the continuation of box C.

☐ Patent family members are listed in annex.

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ANNEXE

au rapport de recherche inter-
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La présente annexe indique les membres de la famille de brevets relatifs aux documents de brevets cités dans le rapport de recherche international visée ci-dessus. Les renseignements fournis sont donnés à titre indicatif et n'engagent pas la responsabilité de l'Office.

| Im Recherchenbericht angeführtes Patentedokument Patent document cited in search report Document de brevet cité dans le rapport de recherche | Datum der Veröffentlichung Publication date Date de publication | Mitglied(er) der Patentfamilie Patent family member(s) Membre(s) de la famille de brevets | Datum der Veröffentlichung Publication date Date de publication |
|---|--|--|--|
| US A 5128782 | 07-07-92 | US A 5416496 | 16-05-95 |
| EP A2 287034 | 19-10-88 | DE 3903776 | 03-03-91 |
| | | DE 3903777 | 03-03-91 |
| | | DE 3903778 | 03-03-91 |
| | | DE 3903779 | 03-03-91 |
| | | DE 3903780 | 03-03-91 |
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